

Subject card

Subject name and code	Design Methodology and Manufacturing, PG_00048073								
Field of study	Electronics and Telecommunications								
Date of commencement of studies	October 2020		Academic year of realisation of subject			2022/2023			
Education level	first-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	3		Language of instruction			Polish			
Semester of study	5		ECTS credits			1.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Department of Metrology and Optoelectronics -> Faculty of Electronics, Telecommunications and In-					nd Informatics			
Name and surname	Subject supervisor	ubject supervisor dr inż. Arkadiusz Szewczyk							
of lecturer (lecturers)	Teachers		dr inż. Arkadiusz Szewczyk dr inż. Jacek Cichosz						
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	:t	Seminar	SUM	
of instruction	Number of study hours	15.0	0.0	0.0	0.0		0.0	15	
	E-learning hours inclu			1		i			
Learning activity and number of study hours	Learning activity	Participation in didactic classes included in study plan		Participation in consultation hours		Self-study		SUM	
	Number of study hours	15		1.0		9.0		25	
Subject objectives	Give the knowledge of technology of design and manufacturing of electronic equipment.								
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Learning outcomes	Course out	come	Subj	ect outcome			Method of verif	fication	
	Course out [K6_W32] Knows the functions and methor design and optimizat analogue and digital electronic systems	parameters, ds of analysis, ion of	knows how to	choose and ap iniques for des			Assessment of		
	[K6_W32] Knows the functions and methodesign and optimizat analogue and digital	e parameters, ds of analysis, ion of circuits and a, according to as, and make ity, system or specific to the suitable a, tools and engineering as, applying to the field of e gained in	knows how to tools and tech electronic dev is able to desi with the given	choose and ap iniques for des	igning nce and	[SW1] A	Assessment of dge	factual	
	[K6_W32] Knows the functions and methodesign and optimizat analogue and digital electronic systems [K6_U03] can design required specification a simple device, facilicarry out a process, field of study, using smethods, techniques materials, following standards and norms technologies specific study and experience the professional engi	e parameters, ds of analysis, ion of circuits and in, according to as, and make ity, system or specific to the suitable to the field of e gained in neering designing and direments of machine and automatic se soldering, flu 12. Surface mounting of elect of solder. 14. Defin and technic ftware. 17. Preg of shields. 19	is able to desi with the given build a simple engineering of anufacturing3. closure and mo th contacts; ma tors and shield soldering. 10. Ir x materials. 11 outning technologiesigning of pad ques of printed paration of fabi . Cooling syste	electronic devirence of the section	ces and mining commining c	[SW1] A knowle [SU1] A fulfilme I system designin hal conn Cabling. iques: shic devicts for the reflow some cuttings. 18. Gr	assessment of dge assessment of ont assessment of assessment of ont assessment of assessment of ont assessment of assessment of ont assessment of ont assessment of ont assess	task of electronic tion en modules: f cables, wrapping, echnology on d surface Production ning, for wrinted board Design hielding	
Learning outcomes	[K6_W32] Knows the functions and method design and optimizat analogue and digital electronic systems [K6_U03] can design required specification a simple device, facilicarry out a process, field of study, using smethods, techniques materials, following standards and normstechnologies specific study and experience the professional engienvironment 1. Basic problems of devices allowing required and separable. If materials for conduction crimping. 9. Manual a environment; lead-free mounting technology units for automatic medepositing of glue and design. 15. Construct Automation (EDA) so techniques. Designing	e parameters, ds of analysis, ion of circuits and in, according to as, and make ity, system or specific to the suitable to the field of e gained in neering designing and direments of machine and automatic se soldering, flu 12. Surface mounting of elect of solder. 14. Defin and technic ftware. 17. Preg of shields. 19	is able to desi with the given build a simple engineering of anufacturing3. closure and mo th contacts; ma tors and shield soldering. 10. Ir x materials. 11 outning technologiesigning of pad ques of printed paration of fabi . Cooling syste	electronic devirence of the section	ces and mining commining c	[SW1] A knowle [SU1] A fulfilme I system designin hal conn Cabling. iques: shic devicts for the reflow some cuttings. 18. Gr	assessment of dge assessment of ont assessment of assessment of ont assessment of assessment of ont assessment of assessment of ont assessment of ont assessment of ont assess	task of electronic tion en modules: f cables, wrapping, echnology on d surface Production ning, for wrinted board Design hielding	

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Assessment methods	Subject passing criteria	Passing threshold	Percentage of the final grade			
and criteria	Writting exam	50.0%	100.0%			
Recommended reading	Basic literature	Ryszard Kisiel: "Podstawy technologii montażu dla elektroników", BTC 2012				
	Supplementary literature	Krystyna Bukart, Halina Hackiewicz: "Lutowanie bezołowiowe", BTC 2007				
	eResources addresses	Adresy na platformie eNauczanie:				
Example issues/ example questions/ tasks being completed	CAD software, soldering, PCB asser	mbly, devices outlines, connections,	grounding, shielding.			
Work placement	Not applicable					

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