

Subject card

Subject name and code	Microcontrollers and Microsystems, PG_00048074							
Field of study	Electronics and Telecommunications							
Date of commencement of studies	October 2020		Academic year of realisation of subject			2022/2023		
Education level	first-cycle studies		Subject group			Optional subject group		
						Subject group related to scientific research in the field of study		
Mode of study	Full-time studies		Mode of delivery			at the university		
Year of study	3		Language of instruction			Polish		
Semester of study	5		ECTS credits			3.0		
Learning profile	general academic profile		Assessment form			assessment		
Conducting unit	Department of Metrology and Optoelectronics -> Faculty of Electronics			onics, T	Telecommunications and Informatics			
Name and surname	Subject supervisor		dr hab. inż. Zbigniew Czaja					
of lecturer (lecturers)	Teachers		dr hab. inż. Zbigniew Czaja					
	dr inż. Bartłomiej Dec							
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	t	Seminar	SUM
of instruction	Number of study hours	30.0	0.0	15.0 0.0			0.0	45
	E-learning hours inclu	uded: 0.0					•	
Learning activity and number of study hours	Learning activity	Participation in didactic classes included in study plan		Participation in consultation hours		Self-study S		SUM
	Number of study hours	45		3.0		27.0		75
	also electronic systems: digital buffers, parallel random access memories, SPLD and CPLD, selected systems controlled via the SPI interface. Acquisition of the ability to analyze ("read") electronic block schemes and timings describing the behavior the system at the time (work in "real time"), as well as effective learning skills of the technical documentation.							e behavior of
Learning outcomes	Course out		Cubi	ant autaoma		Mathad of varification		
Learning outcomes	Course outcome [K6_W04] Knows and understands, to an advanced		Subject outcome Student describes the principle of operation and control of systems			Method of verification [SW1] Assessment of factual		
			that are part of electronic microsystems. Student analyzes program codes written in an assembler and a C language written for microcontrollers.			knowledge		
	[K6_U04] can apply knowledge of programming methods and techniques as well as select and apply appropriate programming methods and tools in computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study		Student explains the construction and principle of operation of the microcontroller and its peripherals. Student describes the principle of operation and control of systems that are part of electronic microsystems. Student uses IDE software for compilation, program simulation and programming of microcontrollers. Student analyzes program codes written in an assembler and a C language written for microcontrollers.			[SU2] Assessment of ability to analyse information [SU1] Assessment of task fulfilment		

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	Lecture: 1. Introduction, plan of the lecture, definition of the microcontroller and features of the core processor 3. Classification of microcontrollers taking into account a memory map (definition of the memory map) and an instruction set 4. Features of the harward architecture and its modifications, proprieties of the Von-Neumann architecture in microcontrollers 5. RISC and CISC architectures of the core processor 6. Internal memories of microcontrollers (program and data memories) 7. Division of the microcontrollers regarding to a way of using of external memories 8. Microcontrollers with access to system buses through ports, with directly access to system buses, embedded microcontrollers 9. A stratified model of the embedded microcontroller 10. Classification and division of families of the microcontrollers 11. Building of an oscillator circuit and applications of circuits of generation and distribution of clock signals 12. Ways of reductions of power consumption and saving power modes of the microcontroller 3. Reset blocks of the microcontroller 14. Units supervising executing of programs by the microcontroller 13. Reset blocks of the microcontroller 14. Units supervising executing of programs by the microcontroller of microcontroller 18. Overview and classification of peripheral devices of the microcontroller 19. Basic information about timers and counters 20. Configurations of timers: 16-bit counter/timer, Input Capture, Output Compare, One Pulse, PVM 21. Examples of the timers: timers in PIC18F452, ST72215G 22. Internal analog to digital converters 23. Internal analog comparators 24. Internal EEPROMs (configuration and service). Example of the EEPROM in Atmega16 25. Characterization and division of serial interface controllers 26. Building, principle of working, controlling of the UART interface 31. Applications of 12C, CAN, USB interfaces 32. The parallel interface PSP 33. Types of packages of the embedded microcontrollers 34. Definition of an embedded programming 35. Programming of the core processor					
Prerequisites and co-requisites	No requirements					
Assessment methods	Subject passing criteria	Passing threshold	Percentage of the final grade			
and criteria	Midterm colloquium	48.0%	60.0%			
	Practical exercise	50.0%	40.0%			
Recommended reading	Basic literature Czaja Z.: Mikrokontrolery i mikrosystemy – materiały do wykładu, http://www.pg.gda.pl/~zbczaja, Gdańsk 2010. Hadam P.: Projektowanie systemów mikroprocesorowych, Wyd. BTC, Warszawa 2004.					
		systemow mikroprocesorowych, wy	d. BTC, Warszawa 2004.			
	Supplementary literature	Bogusz J.: Lokalne interfejsy szereg	gowe w systemach cyfrowych, Wyd. R.: Mikrokontrolery AVR ATmega w 95. Jabłoński T: Mikrokontrolery Varszawa 2002. Jabłoński T., controlerów PIC w języku C, Wyd. R.: Wyświetlacze graficzne i			
	Supplementary literature eResources addresses	Bogusz J.: Lokalne interfejsy szereg BTC, Warszawa 2004. Baranowski praktyce, Wyd. BTC, Warszawa 200 PIC16F8x w praktyce, Wyd. BTC, W Pławsiuk K.: Programowanie mikroł BTC, Warszawa 2005. Baranowski alfanumeryczne w systemach mikro	powe w systemach cyfrowych, Wyd. R.: Mikrokontrolery AVR ATmega w D5. Jabłoński T: Mikrokontrolery Varszawa 2002. Jabłoński T., controlerów PIC w języku C, Wyd. R.: Wyświetlacze graficzne i procesorowych, Wyd. BTC, Vkład i laboratorium 2022/2023			
		Bogusz J.: Lokalne interfejsy szereg BTC, Warszawa 2004. Baranowski praktyce, Wyd. BTC, Warszawa 200 PIC16F8x w praktyce, Wyd. BTC, W Pławsiuk K.: Programowanie mikrok BTC, Warszawa 2005. Baranowski alfanumeryczne w systemach mikrok Legionowo 2008. Adresy na platformie eNauczanie: Mikrokontrolery i mikrosystemy - wy (semestr zimowy) - Moodle ID: 253	powe w systemach cyfrowych, Wyd. R.: Mikrokontrolery AVR ATmega w D5. Jabłoński T: Mikrokontrolery Varszawa 2002. Jabłoński T., controlerów PIC w języku C, Wyd. R.: Wyświetlacze graficzne i procesorowych, Wyd. BTC, Vkład i laboratorium 2022/2023			

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