

## Subject card

Subject name and code	Introduction to electronics and electrotechnics, PG_00051068							
Field of study	Technical Physics							
Date of commencement of studies	October 2021		Academic year of realisation of subject			2022/2023		
Education level	first-cycle studies		Subject group			Obligatory subject group in the field of study Subject group related to scientific research in the field of study		
Mode of study	Full-time studies		Mode of delivery			at the university		
Year of study	2		Language of instruction			Polish		
Semester of study	4		ECTS credits			5.0		
Learning profile	general academic profile		Assessment form			assessment		
Conducting unit	Zakład właściwości magnetycznych i elektrycznych materiałów -> Instytut Nanotechnologii i Inżynierii Materiałowej -> Faculty of Applied Physics and Mathematics							
Name and surname of lecturer (lecturers)	Subject supervisor	dr hab. inż. Ryszard Barczyński						
	Teachers		dr inż. Marek Chmielewski					
			dr hab. inż. Ryszard Barczyński					
	dr inż. Bartosz Trawiński							
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	:t	Seminar	SUM
of instruction	Number of study hours	30.0	0.0	15.0	15.0		0.0	60
	E-learning hours included: 0.0							
Learning activity and number of study hours	Learning activity Participation in classes include plan				Self-study SUM			
	Number of study 60 hours			5.0		60.0		125
Subject objectives	The aim of the course is to teach students the basics of electronics and electrical engineering, as well as basic skills in the design and testing of simple electronic circuits.							
Learning outcomes	Course outcome		Subject outcome			Method of verification		
	K6_W06		He or she knows the basic laws governing electronics. He or she distinguishes between the main types of electronic components.			[SW1] Assessment of factual knowledge		
	-		He or she knows the structure and principle of operation basic instruments for electronic circuit testing.			[SW1] Assessment of factual knowledge		
	K6_U04		He or she independently plans and performs laboratory measurements of electrical quantities in accordance with the received guidelines. He or she performs a critical analysis of the obtained measurement results and draws conclusions from them.			[SU1] Assessment of task fulfilment		
	K6_U06		He or she can estimate the cost of purchasing the components needed to build the designed electronic circuit.			[SU2] Assessment of ability to analyse information		
	K6_W05		He or she has knowledge about a selected computer program for simulating electrical circuits.			[SW3] Assessment of knowledge contained in written work and projects		
	K6_U05		He or she can design and test an analog circuit fulfilling a specific function in a simulation environment.			[SU5] Assessment of ability to present the results of task		

Data wydruku: 10.05.2024 05:22 Strona 1 z 2

Subject contents	<ol> <li>Basics laws of electricity and electronic components</li> <li>Classification of electronic components</li> <li>Schemes of electronic circuits</li> <li>DC electronic circuits</li> <li>AC electronic circuits</li> <li>Basic passive components (RLC)</li> <li>Active components</li> <li>Semiconductors</li> <li>Diodes</li> <li>Transistors</li> <li>Special semiconductor devices</li> <li>Manufacturing of semiconductor devices</li> <li>Integrated circuits</li> <li>Safe exploitation of electrical devices</li> </ol>						
Prerequisites and co-requisites	No prerequisites						
Assessment methods	Subject passing criteria	Passing threshold	Percentage of the final grade				
and criteria	Assessment of the implementation of laboratory exercises	51.0%	20.0%				
	Cost estimate for the purchase of elements for the construction of the designed electronic circuit	51.0%	5.0%				
	Final exam (90 min.)	51.0%	50.0%				
	Report on the simulation of an electrical circuit	51.0%	5.0%				
	Test of knowledge about instruments used in testing electrical circuits placed on the ecourse (15 min.)	51.0%	5.0%				
	Report presenting the results of the project	51.0%	15.0%				
Recommended reading	Basic literature	2015.     W. Opydo, Elektrotechnika i ele nieelektrycznych, Wyd. Politec	VSiP, Warszawa, 2006. rcinkowski, Podstawy I. Politechniki Gdańskiej, Gdańsk, ktronika dla studentów wydziałów niki Poznańskiej, Poznań, 2005. anie: https://enauczanie.pg.edu.pl/				
	Supplementary literature	<ol> <li>P. Hempowicz et al., Elektrotechnika i elektronika dla nieelektryków, WN-T, Warszawa, 1999.</li> <li>P. Horowitz, W. Hill, Sztuka elektroniki 1, WKŁ, Warszawa, 2018.</li> <li>M. Polowczyk, A. Jurewicz, Elektronika dla mechaników, Wyd. Politechniki Gdańskiej, Gdańsk 2002.</li> <li>R. Śledziewski, Elektronika dla fizyków, PWN, Warszawa, 1982.</li> </ol>					
	eResources addresses	Adresy na platformie eNauczanie:					
Example issues/ example questions/ tasks being completed	<ol> <li>Describe nad illustrate Kirchhoff's first law.</li> <li>Build an RC low pass filter and determine its cut-off frequency.</li> <li>Design, build and perform tests of a rumble metal detector.</li> </ol>						
Work placement	Not applicable						

Data wydruku: 10.05.2024 05:22 Strona 2 z 2