

。 GDAŃSK UNIVERSITY OF TECHNOLOGY

Subject card

Subject name and code	Electronic Circuits - laboratory, PG_00049312								
Field of study	Biomedical Engineering, Biomedical Engineering, Biomedical Engineering								
Date of commencement of studies	October 2022		Academic year of realisation of subject			2023/2024			
Education level			Subject group			Obligatory subject group in the field of study			
						Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	2		Language of instruction			Polish			
Semester of study	4		ECTS credits			1.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Department of Microe	Department of Microelectronic Systems -> Faculty of Electronics, Telecommunications and Informatics						formatics	
Name and surname	Subject supervisor		dr hab. inż. Jacek Jakusz						
of lecturer (lecturers)	Teachers		dr hab. inż. Jacek Jakusz						
Lesson types and methods of instruction	Lesson type	Lecture	Tutorial	Laboratory	Projec	t	Seminar	SUM	
	Number of study hours	0.0	0.0	15.0	0.0		0.0	15	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity	Participation in classes includ plan		Participation in consultation hours		Self-study		SUM	
	Number of study hours	15	1.0			9.0		25	
Subject objectives	knowledge of instruments and retention of theoretical knowledge on the structure and properties of electronic systems of linear electronic circuits in the student's lab.								
Learning outcomes	Course outcome		Subject outcome			Method of verification			
	complex and non-typical problems related to the field of study and perform tasks, in an innovative way, in not entirely predictable conditions, by:n- appropriate					[SU4] Assessment of ability to use methods and tools [SU1] Assessment of task fulfilment			
	required specifications, and make a simple device, facility, system or carry out a process, specific to the field of study, using suitable methods, techniques, tools and materials, following engineering standards and norms, applying technologies specific to the field of study and experience gained in the professional engineering environment					[SU4] Assessment of ability to use methods and tools [SU1] Assessment of task fulfilment			
Subject contents	 CMOS programmable array for analog circuit applications Basic gain stages for MOS amplifiers Amplifier with negative feedback loop Bipolar wideband amplifiers Differential pair amplifier Phase-locked oscillator (PLL) Diode rectifier and voltage regulator Buck DC/DC converter 								

Prerequisites and co-requisites	Is required to pass the lecture "ELECTRONIC CIRCUITS" on the 3rd semester					
Assessment methods and criteria	Subject passing criteria	Passing threshold	Percentage of the final grade			
	Practical exercise	50.0%	100.0%			
Recommended reading	Basic literature	Guziński A: "Liniowe elektroniczne układy analogowe", WNT, 1994 Tietze U., Schenk Ch.:"Układy półprzewodnikowe", WNT2009 Sedra A.S., Smith K.C.: "Microelectronic circuits", Oxford University Press, New York, Oxford, 2004				
	Supplementary literature	No requirements				
	eResources addresses	Adresy na platformie eNauczanie:				
Example issues/ example questions/ tasks being completed						
Work placement	Not applicable					

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