



Subject card

Subject name and code	Design Methodology and Manufacturing - laboratory, PG_00048090						
Field of study	Electronics and Telecommunications						
Date of commencement of studies	October 2022	Academic year of realisation of subject			2024/2025		
Education level	first-cycle studies	Subject group			Optional subject group Subject group related to scientific research in the field of study		
Mode of study	Full-time studies	Mode of delivery			at the university		
Year of study	3	Language of instruction			Polish		
Semester of study	6	ECTS credits			2.0		
Learning profile	general academic profile	Assessment form			assessment		
Conducting unit	Department of Metrology and Optoelectronics -> Faculty of Electronics, Telecommunications and Informatics						
Name and surname of lecturer (lecturers)	Subject supervisor	dr inż. Arkadiusz Szewczyk					
	Teachers	dr inż. Arkadiusz Szewczyk					
Lesson types and methods of instruction	Lesson type	Lecture	Tutorial	Laboratory	Project	Seminar	SUM
	Number of study hours	0.0	0.0	30.0	0.0	0.0	30
	E-learning hours included: 0.0						
Learning activity and number of study hours	Learning activity	Participation in didactic classes included in study plan		Participation in consultation hours		Self-study	SUM
	Number of study hours	30		2.0		18.0	50
Subject objectives	Practical learning of technology of design and manufacturing of electronic equipment.						
Learning outcomes	Course outcome	Subject outcome			Method of verification		
	[K6_U07] can apply methods of process and function support, specific to the field of study	can use the methods of computer aided design and analysis of electronic devices			[SU4] Assessment of ability to use methods and tools		
	[K6_U06] can analyse the operation of components, circuits and systems related to the field of study, measure their parameters and examine technical specifications	is able to analyze the operation of the designed device and its components and measure their parameters and examine technical characteristics			[SU2] Assessment of ability to analyse information		
	[K6_U03] can design, according to required specifications, and make a simple device, facility, system or carry out a process, specific to the field of study, using suitable methods, techniques, tools and materials, following engineering standards and norms, applying technologies specific to the field of study and experience gained in the professional engineering environment	is able to design, in accordance with the given specification, and build a simple electronic device			[SU1] Assessment of task fulfilment		

Subject contents	<p>Students in 2-3-person laboratory groups choose an electronic circuit for realisation (common for a laboratory group) with its electrical schematic and requirements, functional description and basic electrical parameters. The introductory exercise is realised by all the students and gives a possibility of learning of current version of software used and is performed strictly according to the description in the laboratory instruction. The results of this exercise are acquired to the laboratory data base.</p> <p>Then students collect components necessary for the realisation of a chosen circuit, define its basic electrical and technological parameters necessary in the designing.</p> <p>The scope of the laboratory program contains accomplishment using software PADS v. 9.5 of Mentor Graphics a schematic, netlist, bill of materials and PCB design together with appropriate reports. After the PCB realisation students make mounting of a chosen circuit (in the SMD or through-holes technology), prepare a program of measurements. The following circuits are available for selection::</p> <ol style="list-style-type: none"> <li>1. Low-noise transistor amplifier</li> <li>2. Generator of rectangular and triangular waveforms</li> <li>3. Voltage-frequency converter</li> <li>4. Power supply with feedback for +15 V</li> <li>5. Acoustic amplifier 2 x 10 W</li> <li>6. Circuit with AT89C2051 microcontroller for LED diodes</li> <li>7. Circuit with AT89C2051 microcontroller for 7-segment display</li> <li>8. Tone generator</li> <li>9. Universal power module</li> <li>10. Bandpass filter with multi-feedback</li> </ol>								
Prerequisites and co-requisites	<p>For a proper realisation of tasks in the laboratory program, students should use a knowledge acquired from the following subjects: <i>Methods and techniques of designing and realisation</i> (lecture), <i>Material engineering, Metrology and technique of an experiment</i>, Analog and digital techniques.</p>								
Assessment methods and criteria	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 50%;">Subject passing criteria</th> <th style="width: 25%;">Passing threshold</th> <th style="width: 25%;">Percentage of the final grade</th> </tr> </thead> <tbody> <tr> <td>Individual and group reports, realised subassembly</td> <td>50.0%</td> <td>100.0%</td> </tr> </tbody> </table>			Subject passing criteria	Passing threshold	Percentage of the final grade	Individual and group reports, realised subassembly	50.0%	100.0%
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Recommended reading	<p>Basic literature</p>	<ol style="list-style-type: none"> <li>1. Laboratory instruction for PADS software.</li> <li>2. Laboratory documentation of software and auxiliary files from producers – <i>User Manuals, Tutorials, data sheets</i>.</li> <li>3. Spiralski L., Konczakowska A.: Basics of technology and construction of electronic equipment and systems. WSM, Gdynia 1997. In Polish.</li> <li>4. 4. Stępień St. and all: Guide for a constructor of electronic equipment. WKiŁ, Warszawa 1981. In Polish.</li> </ol>							
	<p>Supplementary literature</p>	<p>Brak</p>							
	<p>eResources addresses</p>	<p>Adresy na platformie eNauczenie:  Metody Projektowania i Technika Realizacji - laboratorium 2024/25 - Moodle ID: 42919  <a href="https://enauczenie.pg.edu.pl/moodle/course/view.php?id=42919">https://enauczenie.pg.edu.pl/moodle/course/view.php?id=42919</a></p>							
Example issues/ example questions/ tasks being completed	<p>Initial determination of basic electrical parameters and design and technology elements which knowledge is required in the design phase.</p>								
Work placement	<p>Not applicable</p>								

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