

Subject card

Subject name and code	Microcontrollers and Microsystems, PG_00048074							
Field of study	Electronics and Telecommunications							
Date of commencement of studies	October 2022		Academic year of realisation of subject			2024/2025		
Education level	first-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study		
Mode of study	Full-time studies		Mode of delivery			at the	university	
Year of study	3		Language of instruction			Polish		
Semester of study	5		ECTS credits			3.0		
Learning profile	general academic profile		Assessment form			assessment		
Conducting unit	Department of Metrol	ogy and Optoe	lectronics -> F	aculty of Electr	onics, T	elecom	munications	and Informatics
Name and surname	Subject supervisor		dr hab. inż. Zbigniew Czaja					
of lecturer (lecturers)	Teachers		dr hab. inż. Zbigniew Czaja					
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	oratory Project		Seminar	SUM
of instruction	Number of study hours	30.0	0.0	15.0	0.0	0.0 0.0		45
	E-learning hours inclu	uded: 0.0						
Learning activity and number of study hours	Learning activity	Participation i classes including plan		Participation in consultation hours		Self-study		SUM
	Number of study hours	45		3.0		27.0		75
	also electronic systems: digital buffers, parallel random access memories, SPLD and CPLD, systems controlled via the SPI interface. Acquisition of the ability to analyze ("read") electronic block schemes and timings describing the system at the time (work in "real time"), as well as effective learning skills of the technical							
Learning outcomes	Course out	come	Subject outcome			Method of verification		
	[K6_W04] knows and understands, to an advanced extent, the principles, methods and techniques of programming and the principles of computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study, and organisation of systems using computers or such devices		Student describes the principle of operation and control of systems that are part of electronic microsystems. Student analyzes program codes written in an assembler and a C language written for microcontrollers.			[SW1] Assessment of factual knowledge		
	[K6_U04] can apply knowledge of programming methods and techniques as well as select and apply appropriate programming methods and tools in computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study		Student explains the construction and principle of operation of the microcontroller and its peripherals. Student describes the principle of operation and control of systems that are part of electronic microsystems. Student uses IDE software for compilation, program simulation and programming of microcontrollers. Student analyzes program codes written in an assembler and a C language written for microcontrollers.			[SU2] Assessment of ability to analyse information [SU1] Assessment of task fulfilment		

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PIC18F452, ST72215G 22, Internal analog to digital converters 23, Internal analog comparators 24, Internal EEPROMs (Configuration and asevice), Example of the EEPROM in Atmegal 625. Characterization and division of serial interface controllers 26. Building, principle of working, controlling of the UART interface 27. Solutions of the UART interface in microcontrollers: 80.F102, ATmegal 6, PIC18F452 28. The 95P interface service 29. Solutions of the SPI interface in the microcontrollers. Atmegal 6, PIC18F452 28. The 95P interface service 29. Solutions of the SPI interface in the microcontrollers. Atmegal 6, PIC18F452 28. The 95P interface service 29. Solutions of the SPI interface in the microcontrollers with recommendation of the controller of the embedded interface 10. The parallel interface PSP 33. Types of packages of the embedded interface 10. The parallel interface PSP 33. Types of packages of the embedded interface 10. The parallel interface PSP 33. Types of packages of the embedded interface 10. The parallel interface 29. Definition of an embedded processor in the parallel package of the embedded processor in the parallel package of the embedded processor interface 24. Definition of a most package of the embedded processor interface 24. Definition of addressing of external interface units 24. Interface interfaces 15. Interface 24. Definition of addressing of external interface 24. Definition of the microsystems service of crusts in microsystems 2. Definition of the package of the parallel package 20. Analog to digital converters and packages. Did converters with the SPI interface 49. Digital to analog converters: DAI converters, digital temperature sensors, capacitance to digital converters with the SPI interface 49. Digital to analog converters: DAI converters, digital temperature sensors, capacitance to digital converters with the SPI interface 49. Digital to analog converters: DAI converters, digital potentioned by the SPI interface 49. Digital to analog converters: DAI converters with the SPI interf								
Prerequisites and co-requisites Assessment methods and criteria Subject passing criteria Passing threshold Percentage of the final grade	Subject contents	processor 2. Addressing modes of the core processor 3. Classification of microcontrollers taking into account a memory map (definition of the memory map) and an instruction set 4. Features of the harward architecture and its modifications, proprieties of the Von-Neumann architecture in microcontrollers 5. RISC and CISC architectures of the core processor 6. Internal memories of microcontrollers (program and data memories) 7. Division of the microcontrollers agarding to a way of using of external memories 8. Microcontrollers with access to system buses through ports, with directly access to system buses, embedded microcontrollers 9. A stratified model of the embedded microcontroller 10. Classification and division of families of the microcontrollers 11. Building of an oscillator circuit and applications of circuits of generation and distribution of clock signals 12. Ways of reductions of power consumption and saving power modes of the microcontroller 13. Reset blocks of the microcontroller 14. Units supervising executing of programs by the microcontroller, generation of clock signal, supply voltage 15. The watchdog circuit 16. An interrupt system with program polling of devices and a vector interrupt system 17. Parallel ports of the microcontroller 19. Basic information about timers and counters 20. Configurations of timers: 16-bit counter/timer, Input Capture, Output Compare, One Pulse, PWM 21. Examples of the timers: timers in PIC18F452, ST72215G 22. Internal analog to digital converters 23. Internal analog comparators 24. Internal EEPROMs (configuration and service). Example of the EEPROM in Atmega16 25. Characterization and division of serial interface controllers 26. Building, principle of working, controlling of the UART interface 27. Solutions of the UART interface in microcontrollers: 80C51/52, ATmega16, PIC18F452 30. The 1-Wire interface 31. Applications of 12C, CAN, USB interfaces 32. The parallel interface PSP 33. Types of packages of the embedded microcontrollers 34. Definition of an embedded program						
Assessment methods and criteria Subject passing criteria		No requirements						
Midterm colloquium	·		T _	<u> </u>				
Practical exercise 50.0% 40.0%								
Recommended reading Basic literature Czaja Z.: Mikrokontrolery i mikrosystemy – materiały do wykładu, http://www.pg.gda.pl/~zbczaja, Gdańsk 2010. Hadam P.: Projektowanie systemów mikroprocesorowych, Wyd. BTC, Warszawa 2004. Supplementary literature Bogusz J.: Lokalne interfejsy szeregowe w systemach cyfrowych, Wyd. BTC, Warszawa 2004. Baranowski R.: Mikrokontrolery AVR ATmega w praktyce, Wyd. BTC, Warszawa 2005. Jabłoński T.: Mikrokontrolery PlC16F8x w praktyce, Wyd. BTC, Warszawa 2002. Jabłoński T., Pławsiuk K.: Programowanie mikrokontrolerów PlC w języku C, Wyd. BTC, Warszawa 2005. Baranowski R.: Wyświetlacze graficzne i alfanumeryczne w systemach mikroprocesorowych, Wyd. BTC, Legionowo 2008. eResources addresses Adresy na platformie eNauczanie:	and Chlena							
www.pg.gda.pl/~zbczaja, Gdańsk 2010. Hadam P.: Projektowanie systemów mikroprocesorowych, Wyd. BTC, Warszawa 2004. Supplementary literature Bogusz J.: Lokalne interfejsy szeregowe w systemach cyfrowych, Wyd. BTC, Warszawa 2004. Baranowski R.: Mikrokontrolery AVR ATmega w praktyce, Wyd. BTC, Warszawa 2005. Jabłoński T. Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Warszawa 2002. Jabłoński T., Pławsiuk K.: Programowanie mikrokontrolerów PIC w języku C, Wyd. BTC, Warszawa 2005. Baranowski R.: Wyświetlacze graficzne i alfanumeryczne w systemach mikroprocesorowych, Wyd. BTC, Legionowo 2008. eResources addresses Adresy na platformie eNauczanie: Example issues/ example questions/ tasks being completed		Practical exercise						
BTC, Warszawa 2004. Baranowski R.: Mikrokontrolery AVR ATmega w praktyce, Wyd. BTC, Warszawa 2005. Jabłoński T: Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Warszawa 2002. Jabłoński T., Pławsiuk K.: Programowanie mikrokontrolerów PIC w języku C, Wyd. BTC, Warszawa 2005. Baranowski R.: Wyświetlacze graficzne i alfanumeryczne w systemach mikroprocesorowych, Wyd. BTC, Legionowo 2008. eResources addresses Adresy na platformie eNauczanie: Example issues/ example questions/ tasks being completed	Recommended reading	Basic literature	www.pg.gda.pl/~zbczaja, Gdańsk 20	gda.pl/~zbczaja, Gdańsk 2010. Hadam P.: Projektowanie				
Example issues/ example questions/ tasks being completed			BTC, Warszawa 2004. Baranowski R.: Mikrokontrolery ÁVR ÁTmega v praktyce, Wyd. BTC, Warszawa 2005. Jabłoński T: Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Warszawa 2002. Jabłoński T., Pławsiuk K.: Programowanie mikrokontrolerów PIC w języku C, Wyd. BTC, Warszawa 2005. Baranowski R.: Wyświetlacze graficzne i alfanumeryczne w systemach mikroprocesorowych, Wyd. BTC,					
	example questions/	eResources addresses	Adresy na platformie eNauczanie:					
Not applicable	tasks being completed							
WOLK DISCELLENT INOT APPLICABLE	Work placement	Not applicable						

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