

Subject card

Subject name and code	Introduction to low level programming, PG_00037349							
Field of study	Technical Physics							
Date of commencement of	October 2022	Academic v	vear of		2024/2025			
studies	33.000. 2022		Academic year of realisation of subject			2024/2020		
Education level	first-cycle studies		Subject group			Optional subject group		
						Subject group related to scientific research in the field of study		
Mode of study	Full-time studies		Mode of delivery			at the university		
Year of study	3		Language of instruction			Polish		
Semester of study	6		ECTS credits			7.0		
Learning profile	general academic profile		Assessment form			exam		
Conducting unit	Department of Solid S	> Faculty of Applied Physics and Mathematics						
Name and surname	Subject supervisor		dr hab. inż. Ryszard Barczyński					
of lecturer (lecturers)	Teachers		dr inż. Marek Chmielewski					
		dr hab. inż. Ryszard Barczyński						
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	t	Seminar	SUM
of instruction	Number of study hours	30.0	0.0	45.0	0.0		0.0	75
	E-learning hours included: 0.0							
Learning activity and number of study hours	Learning activity Participation in classes include plan					Self-study		SUM
	Number of study hours 75		15.0		85.0		175	
Subject objectives	The aim is to learn architecture and applications of microcontrollers.							
Learning outcomes	Course out	come	Subject outcome Method of verification					
	K6_U03		assembler language and learns to use the microcontroller structure. He can use cross-programming on			[SU1] Assessment of task fulfilment [SU3] Assessment of ability to use knowledge gained from the subject		
	K6_K01		The student learns the peculiarities of microcontroller architecture and their evolution.			[SK5] Assessment of ability to solve problems that arise in practice		
	K6_W05		program a simple measurement and control system.			[SW1] Assessment of factual knowledge [SW3] Assessment of knowledge contained in written work and projects		
Subject contents	* Architecture and applications of microcontrollers. * Hardware of 8051 series microcontrollers. * Structure of assembly languages. Macrodefinitions. * 8051 assembly language. * Architecture and programming of Cypress PSoC I microcontrollers. * Local microcontroller interfaces: SPI, I2C, 1-wire. * Standard I/O expansion circuits. A/D and D/A conversions.							
Prerequisites and co-requisites	No requirements							
Assessment methods	Subject passing criteria		Pass	Passing threshold		Percentage of the final grade		final grade
and criteria	Practical exercise		51.0%		50.0%			
	Midterm colloquium		51.0% 50.0%					
Recommended reading	Basic literature		* Piotr Gałka, Paweł Gałka, Podstawy programowania mikrokontrolera 8051. * Jacek Bogusz, Lokalne interfejsy szeregowe w systemach cyfrowych, btc, Warszawa 2004. * Jacek Bogusz, Programowanie mikrokontrolerów 8051 w języku C w praktyce, btc, Warszawa, 2005. * Materiały do wykładów na stronie WWW * Materiały dotyczące mikrokontrolera PSoC na stronie WWW firmy Cypress www.cypress.com.					

	Supplementary literature	No requirements
	eResources addresses	Adresy na platformie eNauczanie: Wstęp do programowania niskiego poziomu 2025 - Moodle ID: 44197 https://enauczanie.pg.edu.pl/moodle/course/view.php?id=44197
Example issues/ example questions/ tasks being completed	Desing circuit of a matrix keyboard.	
Work placement	Not applicable	

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