

GDAŃSK UNIVERSITY OF TECHNOLOGY

Subject card

Subject name and code	Electronics, PG_00038435								
Field of study	Electrical Engineering								
Date of commencement of studies	October 2021		Academic year of			2022/2023			
	first avela studios		realisation of subject						
Education level	first-cycle studies		Subject group			at the	university		
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	2		Language of instruction			Polish			
Semester of study	3		ECTS credits			5.0			
Learning profile	general academic profile		Assessment form			exam			
Conducting unit		r Electronics ar	nd Electrical Machines -> Faculty of Electrical and Control Engineering						
Name and surname	Subject supervisor		prof. dr hab. inż. Piotr Chrzan						
of lecturer (lecturers)	Teachers		dr hab. inż. Marek Turzyński						
			dr hab. inż. Piotr Musznicki prof. dr hab. inż. Piotr Chrzan						
Number of study hours	30.0	0.0	30.0	0.0		0.0	60		
	E-learning hours incl	uded: 0.0							
Learning activity and number of study hours	Learning activity	Participation i classes includ plan		Participation in consultation hours		Self-study		SUM	
	Number of study hours 60			8.0		57.0		125	
Subject objectives	Knowledge and anal	ysis of fundame	ental electronic	components a	nd appli	cations	•		
Learning outcomes	Course outcome Subject outcome Method of verification								
	K6_W04		Student is able to explain and knows physical mechanisms of phenomena occurring in semiconductor materials.			[SW1] Assessment of factual knowledge [SW3] Assessment of knowledge contained in written work and projects			
	K6_W05		Student is able to perform tasks and laboratory measurements.			[SW1] Assessment of factual knowledge [SW3] Assessment of knowledge contained in written work and projects			
	K6_U08		Student knows operation principles of elements and elementary electronic circuits. Student is able to define functions of electronic system and can design a simple electronic circuit.			[SU3] Assessment of ability to use knowledge gained from the subject [SU4] Assessment of ability to use methods and tools			
	K6_K05		Student knows electrical safety rules of using electronic equipment.			[SK5] Assessment of ability to solve problems that arise in practice			
Subject contents	Laboratory equipment: multimeters, oscilloscopes, measuring probes. Passive electronic components: resistors, capacitors, inductors. Semiconductors: conduction processes, doped semiconductors, pn junction, ms junction. Diodes: switching, rectifier, Schottky, Zener, photodiodes, light emitting diodes, solar panels. Transistors bipolar and unipolar: structure, operation principles, electrical data and characteristics. Optoelectronic components. Amplifiers: Filters. Power amplifiers. Generators. Power supply units. Phase lock loop. Digital circuit technologies. A/C and D/C converters.								
Prerequisites and co-requisites	Fundamentals of physics. Basic circuit theory.								
Assessment methods and criteria	Subject passing criteria		Passing threshold			Percentage of the final grade			
	Written test		50.0%			50.0%			
	Practical exercises		50.0%						

Recommended reading	Basic literature	 <u>Opolski A.: Elektronika dla elektryków. Wydawnictwo PG,</u> <u>Biblioteka Cyfrowa PG, 2008.</u> Opolski A. (red.): Elektronika dla elektryków - Laboratorium. Wydawnictwo PG. Gdańsk 2000. 			
	Supplementary literature	 Hennel J.: Podstawy elektroniki półprzewodnikowej. WNT Warszawa 2003. Boksa J.: Analogowe układy elektroniczne. Wydawnictwo BTC Warszawa 2007. Filipkowski A.: Układy elektroniczne analogowe i cyfrowe. WNT Warszawa 2006. 			
	eResources addresses	Adresy na platformie eNauczanie:			
Example issues/ example questions/ tasks being completed	Field-effect transistors: structure, classification, graphic symbols and current-voltage output characteristics				
Work placement	Not applicable				