

关。GDAŃSK UNIVERSITY 多 OF TECHNOLOGY

Subject card

Subject name and code	Continuous-Time Integrated Filters , PG_00048578									
Field of study	Electronics and Telecommunications									
Date of commencement of studies			Academic year of realisation of subject			2022/2023				
Education level	second-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study				
Mode of study	Full-time studies		Mode of delivery			at the	at the university			
Year of study	1		Language of instruction			Polish	Polish			
Semester of study	1		ECTS credits			2.0				
Learning profile	general academic profile		Assessment form			exam	exam			
Conducting unit	Department of Microe	Department of Microelectronic Systems -> Faculty of Electronics, Telecommunications and Informatics								
Name and surname	Subject supervisor	dr hab. inż. Bo	hab. inż. Bogdan Pankiewicz							
of lecturer (lecturers)	Teachers		dr hab. inż. Bogdan Pankiewicz							
Lesson types and methods of instruction	Lesson type	Lecture	Tutorial	Laboratory	Projec	t	Seminar	SUM		
	Number of study hours	15.0	0.0	15.0	0.0		0.0	30		
	E-learning hours included: 0.0									
Learning activity and number of study hours	Learning activity	Participation in classes includ plan		Participation in consultation hours		Self-study		SUM		
	Number of study hours	Number of study 30		4.0		16.0 50		50		
Subject objectives	Analysis and design of continuous time integrated filters.									
Learning outcomes	Course outcome		Subject outcome			Method of verification				
	required specifications, and make a complex device, facility, system or carry out a process, specific to		K_U30 Can design continuous- time integrated filter of second or higher order. Can verify design of the filter using PSPICE simulations.			[SU1] Assessment of task fulfilment				
	formulating engineering tasks specifications and solving these tasks, can:n- apply analytical, simulation and experimental methods,n- notice their systemic and non-technical aspects,n- make a preliminary economic assessment of suggested solutions and engineering workn [K7_W03] Knows and understands, to an increased extent, the construction and		K_U30 Can design continuous- time integrated filter of second or higher order. Can verify design of the filter using PSPICE simulations. K_W27 Knows theory regarding approximation of filter frequency responses, knows methods of frequency transformations and synthesis of filters.			[SU1] Assessment of task fulfilment [SW1] Assessment of factual knowledge				

Subject contents	1. Introduction, classification of continuous-time active filters. 2. Building blocks and properties of operational amplifiers (i.e. Amps, OTAs and operational transresistance amplifiers). 3. Introduction to synthesis of active filters, normalization procedures, frequency transformations, approximation methods. 4. The synthesis of cecond-order active filters. 5. Cascade realizations of high-order filters. 6. Methods for LC ladder simulations. 7. LP-HP frequency transformation. 8.LP- BP frequency transformation.						
Prerequisites and co-requisites							
Assessment methods and criteria	Subject passing criteria	Passing threshold	Percentage of the final grade				
	Written exam	51.0%	75.0%				
	Laboratory exercises	51.0%	25.0%				
Recommended reading	Basic literature	WNT, Warszawa, 1979 2. Schaumann Rolf, Van Valker	 Białko M., Guziński A., Sieńko W., Żurada J, Filtry aktywne RC, WNT, Warszawa, 1979 Schaumann Rolf, Van Valkenburg Mac E., Design of Analog Filters , Oxford University Press, N.Y, 2001 				
	Supplementary literature	Razavi Behzad, Design of Analog CMOS Integrated Circuits, McGraw- Hill, 2003					
	eResources addresses	Adresy na platformie eNauczanie:					
Example issues/ example questions/ tasks being completed							
Work placement	Not applicable	Not applicable					