

Subject card

Subject name and code	Design of Functio nal Blocks for Digital Channels, PG_00048361								
Field of study	Electronics and Telecommunications								
Date of commencement of studies	February 2023		Academic year of realisation of subject			2023/2024			
Education level	second-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	2		Language of instruction			Polish			
Semester of study	3		ECTS credits			1.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Department of Teleinformation Networks -> Faculty of Electronics, Telecommunications and Informatics							formatics	
Name and surname	Subject supervisor	supervisor mgr inż. Jacek Litka							
of lecturer (lecturers)	Teachers		mgr inż. Jacek Litka						
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	t	Seminar	SUM	
of instruction	Number of study hours	0.0	0.0	0.0	15.0		0.0	15	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity	Participation i classes include plan				Self-study		SUM	
	Number of study hours	15		1.0		9.0		25	
Subject objectives	Familiarize students with the practice of design of selected functional blocks of digital channels								
Learning outcomes	Course outcome		Subject outcome			Method of verification			
	[K7_W03] Knows and understands, to an increased extent, the construction and operating principles of components and systems related to the field of study, including theories, methods and complex relationships between them and selected specific issues - appropriate for the curriculum.		In the scope of the selected project task, the student knows and understands the structure of selected functional blocks of the digital channel and the relationship between their parameters and their characteristics and functioning.			[SW2] Assessment of knowledge contained in presentation [SW3] Assessment of knowledge contained in written work and projects			
	[K7_U06] can analyse the operation of components, circuits and systems related to the field of study; measure their parameters; examine technical specifications; interpret obtained results and draw conclusions [K7_W05] Knows and understands, to an increased extent, methods of process and		examines their parameters and characteristics, interprets the results and draws conclusions. In the scope of the selected project task, the student knows			[SU1] Assessment of task fulfilment [SU2] Assessment of ability to analyse information [SU4] Assessment of ability to use methods and tools [SW2] Assessment of knowledge contained in presentation [SW3] Assessment of knowledge			
	function support, specific to the field of study.		designing and testing selected functional blocks of a digital channel.			contained in written work and projects			

Data wydruku: 10.05.2024 15:08 Strona 1 z 2

Subject contents	 Single-parameter modulator. Shaping and receiving filters. Synchronization in single-parameter receiver. Sample rate conversion. Multicarrier modulator. Multicarrier demodulator. Speech parameterizations. 					
Prerequisites and co-requisites						
Assessment methods and criteria	Subject passing criteria	Passing threshold	Percentage of the final grade			
	Project	50.0%	100.0%			
Recommended reading	Basic literature	Fuqin Xiong: Digital Modulation Techniques, Artech House, 2000				
,	Supplementary literature No requirements					
	eResources addresses Adresy na platformie eNauczanie:					
		Projektowanie bloków funkcjonalnych kanałów cyfrowych - 2023/2024 - Moodle ID: 36863 https://enauczanie.pg.edu.pl/moodle/course/view.php?id=36863				
Example issues/ example questions/ tasks being completed						
Work placement	Not applicable					

Data wydruku: 10.05.2024 15:08 Strona 2 z 2