

Subject card

Subject name and code	Introduction to electronics and electrotechnics, PG_00051068								
Field of study	Technical Physics								
Date of commencement of studies	October 2023		Academic year of realisation of subject			2024/2025			
Education level	first-cycle studies		Subject group			Obligatory subject group in the field of study Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	2		Language of instruction			Polish			
Semester of study	4		ECTS credits			5.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Zakład właściwości magnetycznych i elektrycznych materiałów -> Institute of Nanotechnology and Materials Engineering -> Faculty of Applied Physics and Mathematics							and Materials	
Name and surname	Subject supervisor		dr hab. inż. Ryszard Barczyński						
of lecturer (lecturers)	Teachers	dr hab. inż. Ryszard Barczyński							
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	ct Seminar SUM		SUM	
of instruction	Number of study hours	30.0	0.0	15.0	15.0		0.0	60	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity	activity Participation in d classes included plan				Self-study S		SUM	
	Number of study 60 hours		5.0		60.0		125		
Subject objectives	The aim of the course is to teach students the basics of electronics and electrical engineering, as well as basic skills in the design and testing of simple electronic circuits.								
Learning outcomes	Course out		1	ect outcome			Method of ve	rification	
	[K6_W07] Has knowledge of the construction and operation of physical instruments, measurement and research equipment.					[SW1] Assessment of factual knowledge			
	[K6_W06] Has knowledge of electronics.		He or she knows the basic laws governing electronics. He or she distinguishes between the main types of electronic components.			[SW1] Assessment of factual knowledge			
	[K6_U05] Can design and build a simple measuring device		He or she can design and test an analog circuit fulfilling a specific function in a simulation environment.			[SU5] Assessment of ability to present the results of task			
	[K6_U06] Can make an initial economic analysis of undertaken engineering activities.		He or she can estimate the cost of purchasing the components needed to build the designed electronic circuit.			[SU2] Assessment of ability to analyse information			
Subject contents	 Basics laws of electricity and electronic components Classification of electronic components Schemes of electronic circuits DC electronic circuits AC electronic circuits Basic passive components (RLC) Active components Semiconductors Diodes Transistors Special semiconductor devices Manufacturing of semiconductor devices Integrated circuits Safe exploitation of electrical devices 								

Prerequisites and co-requisites	No prerequisites					
Assessment methods	sessment methods Subject passing criteria		Percentage of the final grade			
and criteria	Test of knowledge about instruments used in testing electrical circuits placed on the ecourse (15 min.)	51.0%	5.0%			
	Report presenting the results of the project	51.0%	15.0%			
	Assessment of the implementation of laboratory exercises	51.0%	20.0%			
	Cost estimate for the purchase of elements for the construction of the designed electronic circuit	51.0%	5.0%			
	Final exam (90 min.)	51.0%	50.0%			
	Report on the simulation of an electrical circuit	51.0%	5.0%			
Recommended reading	Basic literature	 A. Chwaleba, B. Moeschke, G. Płoszajski, Elektronika, WSiP, Warszawa, 1999. S. Bolkowski, Elektrotechnika, WSiP, Warszawa, 2006. A. Kloskowski, J. Wawer, Ł. Marcinkowski, Podstawy elektrotechniki i elektroniki, Wyd. Politechniki Gdańskiej, Gdańs 2015. W. Opydo, Elektrotechnika i elektronika dla studentów wydziałó nieelektrycznych, Wyd. Politechniki Poznańskiej, Poznań, 2005 Materials published on e-nauczanie: https://enauczanie.pg.edu.moodle/course/view.php?id=10797 				
	Supplementary literature	 P. Hempowicz et al., Elektrotechnika i elektronika dla nieelektryków, WN-T, Warszawa, 1999. P. Horowitz, W. Hill, Sztuka elektroniki 1, WKŁ, Warszawa, 2018. M. Polowczyk, A. Jurewicz, Elektronika dla mechaników, Wyd. Politechniki Gdańskiej, Gdańsk 2002. R. Śledziewski, Elektronika dla fizyków, PWN, Warszawa, 1982. 				
	eResources addresses	Adresy na platformie eNauczanie:				
		Wstęp do elektroniki i elektrotechniki - Moodle ID: 43996 https://enauczanie.pg.edu.pl/moodle/course/view.php?id=43996				
Example issues/ example questions/ tasks being completed	 Describe nad illustrate Kirchhoff's first law. Build an RC low pass filter and determine its cut-off frequency. Design, build and perform tests of a rumble metal detector. 					
Work placement	Not applicable					

Document generated electronically. Does not require a seal or signature.

Data wygenerowania: 11.03.2025 19:32 Strona 2 z 2