

Subject card

Subject name and code	Microcontrollers and Microsystems, PG_00048074								
Field of study	Electronics and Telecommunications								
Date of commencement of studies	October 2023		Academic year of realisation of subject			2025/2026			
Education level	first-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	3		Language of instruction			Polish			
Semester of study	5		ECTS credits			3.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Department of Metrology and Optoe		lectronics -> Faculty of Electronics, T			elecommunications and Informatics			
Name and surname of lecturer (lecturers)	Subject supervisor		dr hab. inż. Zbigniew Czaja						
	Teachers		dr hab. inż. Zbigniew Czaja						
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	poratory Project		Seminar	SUM	
of instruction	Number of study hours	30.0	0.0	15.0	0.0	0.0		45	
	E-learning hours inclu	uded: 0.0							
Learning activity and number of study hours	Learning activity	Participation i classes including		Participation in consultation hours		Self-study		SUM	
	Number of study hours	45		3.0		27.0		75	
	also electronic systems: digital buffers, parallel random access memories, SPLD and CPLD, selected systems controlled via the SPI interface. Acquisition of the ability to analyze ("read") electronic block schemes and timings describing the behavior of the system at the time (work in "real time"), as well as effective learning skills of the technical documentation.								
Learning outcomes	Course outcome		Subject outcome			Method of verification			
	[K6_U04] can apply knowledge of programming methods and techniques as well as select and apply appropriate programming methods and tools in computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study		Student explains the construction and principle of operation of the microcontroller and its peripherals. Student describes the principle of operation and control of systems that are part of electronic microsystems. Student uses IDE software for compilation, program simulation and programming of microcontrollers. Student analyzes program codes written in an assembler and a C language written for microcontrollers.			[SU1] Assessment of task fulfilment [SU2] Assessment of ability to analyse information			
	[K6_W04] Knows and understands, to an advanced extent, the principles, methods and techniques of programming and the principles of computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study, and organisation of systems using computers or such devices		Student describes the principle of operation and control of systems that are part of electronic microsystems. Student analyzes program codes written in an assembler and a C language written for microcontrollers.			[SW1] Assessment of factual knowledge			

Data wydruku: 19.05.2024 09:37 Strona 1 z 2

		lecture, definition of the microcontrolle				
Subject contents	processor 2. Addressing modes of the core processor 3. Classification of microcontrollers taking into account a memory map (definition of the memory map) and an instruction set 4. Features of the harward architecture and its modifications, proprieties of the Von-Neumann architecture in microcontrollers 5. RISC and CISC architectures of the core processor 6. Internal memories of microcontrollers (program and data memories) 7. Division of the microcontrollers garding to a way of using of external memories 8. Microcontrollers with access to system buses, embedded microcontrollers 9. A stratified model of the embedded microcontroller 19. Classification and division of families of the microcontrollers 11. Building of an oscillator circuit and applications of circuits of generation and distribution of clock signals 12. Ways of reductions of power consumption and saving power modes of the microcontroller 13. Reset blocks of the microcontroller 14. Units supervising executing of programs by the microcontroller, generation of clock signal, supply voltage 15. The watchdog circuit 16. An interrupt system with program polling of devices and a vector interrupt system 17. Paralle ports of the microcontroller - the layer of multiplexers and input/output pins 18. Overview and classification of peripheral devices of the microcontroller 19. Basic information about timers and counters 20. Configurations of timers: 16-bit counter/timer, Input Capture, Output Compare, One Pulse, PWM 21. Examples of the timers: timers in PIC18F452, ST72215G 22. Internal analog to digital converters 23. Internal analog comparators 24. Internal EEPRCMS (configuration and service). Example of the EEPRCM in Atmega16 25. Characterization and division of serial interface controllers 26. Building, principle of working, controlling of the UART interface service 29. Solutions of the UART interface in microcontrollers with the SPI Cl18F452 30. The 1-Wire interface 31. Applications of 12C, CAN, USB interfaces 32. The parallel interface PSP 33. Types of package					
Prerequisites and co-requisites	No requirements					
Assessment methods and criteria	Subject passing criteria	Passing threshold	Percentage of the final grade			
	Practical exercise	50.0%	40.0%			
	Midterm colloquium	48.0%	60.0%			
Recommended reading	Basic literature Czaja Z.: Mikrokontrolery i mikrosystemy – materiały do wykładu, r www.pg.gda.pl/~zbczaja, Gdańsk 2010. Hadam P.: Projektowanie systemów mikroprocesorowych, Wyd. BTC, Warszawa 2004.					
	Supplementary literature	Bogusz J.: Lokalne interfejsy szeregowe w systemach cyfrowych, Wyd. BTC, Warszawa 2004. Baranowski R.: Mikrokontrolery AVR ATmega w praktyce, Wyd. BTC, Warszawa 2005. Jabłoński T: Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Warszawa 2002. Jabłoński T., Pławsiuk K.: Programowanie mikrokontrolerów PIC w języku C, Wyd. BTC, Warszawa 2005. Baranowski R.: Wyświetlacze graficzne i alfanumeryczne w systemach mikroprocesorowych, Wyd. BTC, Legionowo 2008.				
Example issues/	eResources addresses	Adresy na platformie eNauczanie:				
example questions/						
tasks being completed Work placement	Not applicable					

Data wydruku: 19.05.2024 09:37 Strona 2 z 2