

## Subject card

Subject name and code	Introduction to electronics and electrotechnics, PG_00051068								
Field of study	Technical Physics								
Date of commencement of studies	,		Academic year of realisation of subject			2025/2026			
Education level	first-cycle studies		Subject group			Obligatory subject group in the field of study Subject group related to scientific			
						research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	2		Language of instruction			Polish			
Semester of study	4		ECTS credits			5.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Materiałowej -> Facul	akład właściwości magnetycznych i elektrycznych materiałów -> Instytut Nanotechnologii i Inżynierii lateriałowej -> Faculty of Applied Physics and Mathematics						ynierii	
Name and surname	Subject supervisor								
of lecturer (lecturers)	Teachers	I		i	1		i	1	
Lesson types and methods of instruction	Lesson type Number of study hours	Lecture 30.0	Tutorial 0.0	Laboratory 15.0	Project 15.0	t	Seminar 0.0	SUM 60	
	E-learning hours inclu	ıded: 0.0					1		
Learning activity and number of study hours	Learning activity	Participation in classes include plan		Participation in consultation hours		Self-study		SUM	
	Number of study hours	60		5.0		60.0		125	
Subject objectives	The aim of the course is to teach students the basics of electronics and electrical engineering, as well as basic skills in the design and testing of simple electronic circuits.								
Learning outcomes	Course outcome			Subject outcome			Method of verification		
	[K6_W07] has knowledge of the construction and operation of physical instruments, measurement and research equipment		He or she knows the structure and principle of operation basic instruments for electronic circuit testing.			[SW1] Assessment of factual knowledge			
	[K6_W06] has knowledge of electronics		He or she knows the basic laws governing electronics. He or she distinguishes between the main types of electronic components.			[SW1] Assessment of factual knowledge			
	engineering activities		He or she can estimate the cost of purchasing the components needed to build the designed electronic circuit.			[SU2] Assessment of ability to analyse information			
	[K6_U05] designs and builds a simple measuring device		He or she can design and test an analog circuit fulfilling a specific function in a simulation environment.			[SU5] Assessment of ability to present the results of task			
Subject contents	<ol> <li>Basics laws of electricity and electronic components</li> <li>Classification of electronic components</li> <li>Schemes of electronic circuits</li> <li>DC electronic circuits</li> <li>AC electronic circuits</li> <li>Basic passive components (RLC)</li> <li>Active components</li> <li>Semiconductors</li> <li>Diodes</li> <li>Transistors</li> <li>Special semiconductor devices</li> <li>Manufacturing of semiconductor devices</li> <li>Integrated circuits</li> <li>Safe exploitation of electrical devices</li> </ol>								

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Prerequisites and co-requisites	No prerequisites					
Assessment methods	Subject passing criteria	Passing threshold	Percentage of the final grade			
and criteria	Report on the simulation of an electrical circuit	51.0%	5.0%			
	Final exam (90 min.)	51.0%	50.0%			
	Cost estimate for the purchase of elements for the construction of the designed electronic circuit	51.0%	5.0%			
	Assessment of the implementation of laboratory exercises	51.0%	20.0%			
	Report presenting the results of the project	51.0%	15.0%			
	Test of knowledge about instruments used in testing electrical circuits placed on the ecourse (15 min.)	51.0%	5.0%			
Recommended reading	Basic literature	<ul><li>2015.</li><li>4. W. Opydo, Elektrotechnika i ele nieelektrycznych, Wyd. Politecl</li></ul>	WSiP, Warszawa, 2006. arcinkowski, Podstawy d. Politechniki Gdańskiej, Gdańsk, ektronika dla studentów wydziałów hniki Poznańskiej, Poznań, 2005. zanie: https://enauczanie.pg.edu.pl/			
	Supplementary literature	<ol> <li>P. Hempowicz et al., Elektrotechnika i elektronika dla nieelektryków, WN-T, Warszawa, 1999.</li> <li>P. Horowitz, W. Hill, Sztuka elektroniki 1, WKŁ, Warszawa, 2018.</li> <li>M. Polowczyk, A. Jurewicz, Elektronika dla mechaników, Wyd. Politechniki Gdańskiej, Gdańsk 2002.</li> <li>R. Śledziewski, Elektronika dla fizyków, PWN, Warszawa, 1982.</li> </ol>				
	eResources addresses	Adresy na platformie eNauczanie:				
Example issues/ example questions/ tasks being completed	<ol> <li>Describe nad illustrate Kirchhoff's first law.</li> <li>Build an RC low pass filter and determine its cut-off frequency.</li> <li>Design, build and perform tests of a rumble metal detector.</li> </ol>					
Work placement	Not applicable					

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