



## Subject card

|   |  |  |                                     |            |   |         |     |
|---|--|--|-------------------------------------|------------|---|---------|-----|
| Subject name and code                       | Electronics and Electrical Engineering, PG_00064126  |  |                                     |            |   |         |     |
| Field of study                              | Mechanical and Medical Engineering   |  |                                     |            |   |         |     |
| Date of commencement of studies             | October 2024   | Academic year of realisation of subject  |                                     |            | 2025/2026   |         |     |
| Education level                             | first-cycle studies  | Subject group  |                                     |            | Obligatory subject group in the field of study  |         |     |
| Mode of study                               | Full-time studies  | Mode of delivery   |                                     |            | at the university   |         |     |
| Year of study                               | 2  | Language of instruction  |                                     |            | Polish  |         |     |
| Semester of study                           | 4  | ECTS credits   |                                     |            | 4.0   |         |     |
| Learning profile                            | general academic profile   | Assessment form  |                                     |            | assessment  |         |     |
| Conducting unit                             | Institute of Manufacturing and Materials Technology -> Faculty of Mechanical Engineering and Ship Technology -> Faculties of Gdańsk University of Technology   |  |                                     |            |   |         |     |
| Name and surname of lecturer (lecturers)    | Subject supervisor   | prof. dr hab. inż. Dionizy Czekaj  |                                     |            |   |         |     |
|   | Teachers   | mgr inż. Klaudia Malisz-Rudzińska<br>prof. dr hab. inż. Dionizy Czekaj   |                                     |            |   |         |     |
| Lesson types                                | Lesson type  | Lecture  | Tutorial                            | Laboratory | Project   | Seminar | SUM |
|   | Number of study hours  | 30.0   | 0.0                                 | 30.0       | 0.0   | 0.0     | 60  |
|   | E-learning hours included: 0.0   |  |                                     |            |   |         |     |
|   | eNauczanie source addresses:<br>Moodle ID: 2348 Elektronika i elektrotechnika, W, IMM, sem.04 letni 25/26 (PG_00064126)<br><a href="https://enauczanie.pg.edu.pl/2025/course/view.php?id=2348">https://enauczanie.pg.edu.pl/2025/course/view.php?id=2348</a><br>Moodle ID: 2348 Elektronika i elektrotechnika, W, IMM, sem.04 letni 25/26 (PG_00064126)<br><a href="https://enauczanie.pg.edu.pl/2025/course/view.php?id=2348">https://enauczanie.pg.edu.pl/2025/course/view.php?id=2348</a> |  |                                     |            |   |         |     |
| Learning activity and number of study hours | Learning activity  | Participation in didactic classes included in study plan   | Participation in consultation hours | Self-study | SUM   |         |     |
|   | Number of study hours  | 60   | 2.0                                 | 38.0       | 100   |         |     |
| Subject objectives                          | This series of lectures covers the fundamentals of electrical engineering, analog, and digital electronics. Students will become familiar with the fundamentals of electrical engineering, electronic components, and linear circuits based on bipolar and field-effect transistors. Basic logic concepts will be introduced and typical digital circuits will be discussed.   |  |                                     |            |   |         |     |
| Learning outcomes                           | Course outcome   | Subject outcome  |                                     |            | Method of verification  |         |     |
|   | [K6_W04] has knowledge in automation and robotics of mechanical systems or electrical and electronic engineering or thermodynamics and fluid mechanics including bioreology  | The student defines basic physical quantities in electrical circuits. Explains the laws describing the relationships between physical quantities in electrical circuits. |                                     |            | [SW1] Assessment of factual knowledge   |         |     |
|   | [K6_U01] is able to acquire knowledge and self-studying, he/she is able to find needed information in specialist books, databases and other sources, he/she is able to integrate information and draw conclusions, he/she is able to communicate by using different technics in work and outside   | The student operates basic measuring devices. Connects simple electrical circuits. Measures basic electrical quantities. Interprets measurement results.                 |                                     |            | [SU1] Assessment of task fulfilment<br>[SU4] Assessment of ability to use methods and tools<br>[SU3] Assessment of ability to use knowledge gained from the subject |         |     |

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| Subject contents   | Course content – lecture<br>Basic Concepts. Voltage and Current, Ohm's Law, Circuit Diagrams, Capacitor and Resistor Connections, Kirchhoff's Laws, Voltage and Current Dividers. RC and RLC Circuits. Diodes and Diode Circuits. The P-N Junction, Rectifier Diode Characteristics, the Zener Diode, Rectifier Circuits. Bipolar Transistors. Principle of Transistor Operation and Its Basic Parameters, Characteristics of Static Transistors. Field-Effect Transistors (FETs). Types of FETs, Structure and Properties, Basic FE Circuits. Numerical Codes, Logic Gates, and Logical Relationships. Combination Functional Circuits and Circuits Using Sequential Logic. |  |                               |
|  | Course content – laboratory<br>1. Calculating DC circuits using the pSpice simulation program<br>2. Calculating AC circuits<br>3. Determining the characteristics of passive filters<br>4. Studying transients<br>5. Static and dynamic parameters of semiconductor rectifier diodes<br>6. Studying the current-voltage characteristics of a Zener diode<br>7. Analysis of a bipolar transistor  |  |                               |
| Prerequisites and co-requisites                                |  |  |                               |
| Assessment methods and criteria                                | Subject passing criteria   | Passing threshold  | Percentage of the final grade |
|  | Final lecture test   | 50.0%  | 50.0%                         |
|  | Laboratory classes   | 100.0%   | 50.0%                         |
| Recommended reading  | Basic literature   | <ol style="list-style-type: none"> <li>1. P. Horowitz, W. Hill, (2015). The Art of Electronics, Third Edition, New York, Cambridge University Press, (<a href="#">pdf</a>)</li> <li>2. Bird, J. (2003). Electrical Circuit Theory and Technology. Oxford: Newnes, An Impring of Elsevier Science (<a href="#">pdf</a>)</li> <li>3. Ling, S. J., William Moebis, W., &amp; Sanny, J. (2016). <i>University Physics Volume 2</i>. Houston, Texas : OpenStax (<a href="#">available online</a>). Book URL: <a href="https://openstax.org/books/university-physics-volume-2/pages/1-introduction">https://openstax.org/books/university-physics-volume-2/pages/1-introduction</a></li> </ol> |                               |
|  | Supplementary literature   | <ol style="list-style-type: none"> <li>1. K. Wojtuszkiewicz, Z. Zachara, PSpice. Przykłady praktyczne, W-wa, Mikom, 2000</li> <li>2. Z. Zachara, K. Wojtuszkiewicz, PSpice. Symulacje wzmacniaczy dyskretnych, W-wa, Mikom, 2001</li> <li>3. T. Masewicz, Radioelektronika dla praktyków, WKŁ, W-wa, 1986</li> <li>4. W. Głocki, L. Grabowski, Pracownia podstaw techniki cyfrowej, WSiP, W-wa, 1998</li> <li>5. <a href="#">Symulator obwodów elektrycznych</a>: Circuit Simulator version 3.1.3.js. Original by Paul Falstad</li> <li>6. <a href="#">Forbot, Kurs elektroniki dla początkujących</a></li> <li>7. <a href="#">Forbot, Kurs elektroniki II</a></li> </ol>                |                               |
|  | eResources addresses   |  |                               |
| Example issues/<br>example questions/<br>tasks being completed | <ol style="list-style-type: none"> <li>1. Current and voltage dividers</li> <li>2. RC circuits. Differentiator</li> <li>3. Half-wave rectifier with resistive and resistive-capacitive loads.</li> <li>4. Bipolar transistor bias circuits.</li> </ol>   |  |                               |
| Practical activities within the subject                        | Not applicable   |  |                               |

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