

Subject card

Subject name and code	Programmable Logic Controllers and Process Visualization, PG_00047577								
Field of study	Automatic Control, Cybernetics and Robotics								
Date of commencement of studies	October 2024		Academic year of realisation of subject			2025/2026			
Education level	first-cycle studies		Subject group			Obligatory subject group in the field of study			
						Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	2		Language of instruction			Polish			
Semester of study	4		ECTS credits			5.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Department of Decision Systems and Robotics -> Faculty of Electronics, Telecommunications and Informatics								
Name and surname	Subject supervisor		dr hab. inż. Tomasz Talaśka						
of lecturer (lecturers)	Teachers		dr hab. inż. Tomasz Talaśka						
Lesson types and methods of instruction	Lesson type	Lecture	Tutorial	Laboratory	aboratory Project		Seminar	SUM	
	Number of study hours	30.0	0.0	30.0	0.0		0.0	60	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity	g activity Participation in dida classes included in plan		Participation in consultation hours		Self-study S		SUM	
	Number of study hours			5.0		60.0		125	
Subject objectives	Acquainted with programmable logic controllers, their programming and the using in automation. Basic knowledge of supervising, data acquisition and process visualization systems (SCADA).								
Learning outcomes	Course outcome Subject outcome Method of ve					rification			
	decision making as well as		Knowledge about the use of programmable logic controllers in simple and complex automation systems.			[SW1] Assessment of factual knowledge			
	required specifications, and make		Is able to program programmable logic controllers used in simple automation systems. Knowledge about programmable logic controllers (PLC) and about supervisory control and data acquisition systems (SCADA).		[SW1] Assessment of ability to use methods and tools [SW1] Assessment of factual knowledge				

Data wydruku: 18.07.2024 08:45 Strona 1 z 2

Subject contents	1. Preliminary information regarding propriety and application of Programmable Logic Controllers (PLC). 2. General controller architecture, operating system and program cycle. 3. Programming languages used in PLCs. 4. Logicmaster graphical programming language. 5. Basic rules of program creation. 6. Data and variables. 7. Switches, relays and connections. 8. Counters and time-based relays. 9. Mathematical functions and relations. 10. Data manipulation. 11. Control functions. 12. Programming examples. 13. Exemplary PLC hardware modules. 14. Digital inputs and outputs modules. 15. Analog inputs and outputs modules. 16. PLC controllers and networks. 17. Communication protocols. 18. Communication modules. 19. Industrial GENIUS network. 20. Collaboration of networks and PLCs. 21. SCADA (Supervisory Control and Data Acquisition) systems. 22. InTouch - creator of SCADA applications. 23. Windows creation - graphical editor. 24. Variables and animation connections. 25. Scripts. 26. Alarms. 27. Communication with PLCs. 28. Actual and historical trends. 29. Graphics import (Symbol Factory). 30. Ready-to-use applications managing.					
Prerequisites and co-requisites						
Assessment methods and criteria	Subject passing criteria	Passing threshold	Percentage of the final grade			
	evaluation of laboratory	50.0%	60.0%			
	test	50.0%	40.0%			
Recommended reading	Basic literature	T.Legierski, J.Kasprzyk, J.Wyrwał, J.Hajda, "Programowanie sterowników PLC", Wyd. Pracowni Komputerowej J.Skalmierskiego A.Maczyński, "Sterowniki programowalne PLC. Budowa systemu i podstawy programowania. Astor				
	Supplementary literature	No requirements				
	eResources addresses	Adresy na platformie eNauczanie:				
Example issues/ example questions/ tasks being completed						
Work placement	Not applicable					

Data wydruku: 18.07.2024 08:45 Strona 2 z 2