

## Subject card

| Subject name and code                       | Microcontrollers and Distributed Microsystems, PG_00047596   |  |   |                                     |                        |  |                |                |
|---|--|--|---|-------------------------------------|------------------------|--|----------------|----------------|
| Field of study                              | Automatic Control, Cybernetics and Robotics  |  |   |                                     |                        |  |                |                |
| Date of commencement of                     | October 2024   | Academic year of   |   |                                     | 2026/2027              |  |                |                |
| studies                                     |  |  | realisation of subject  |                                     |                        | 2020/2021  |                |                |
| Education level                             | first-cycle studies  |  | Subject group   |                                     |                        | Obligatory subject group in the field of study                     |                |                |
|   |  |  |   |                                     |                        | Subject group related to scientific research in the field of study |                |                |
| Mode of study                               | Full-time studies  |  | Mode of delivery  |                                     |                        | at the university  |                |                |
| Year of study                               | 3  |  | Language of instruction   |                                     |                        | Polish   |                |                |
| Semester of study                           | 5  |  | ECTS credits  |                                     |                        | 2.0  |                |                |
| Learning profile                            | general academic profile   |  | Assessment form   |                                     |                        | assessment   |                |                |
| Conducting unit                             | Department of Metrol   | ogy and Optoe  | lectronics -> Fa  | aculty of Electr                    | onics, T               | elecom   | munications ar | nd Informatics |
| Name and surname of lecturer (lecturers)    | Subject supervisor dr hab. inż. Zbigniew Czaja   |  |   |                                     |                        |  |                |                |
|   | Teachers   | dr hab. inż. Zbigniew Czaja                              |   |                                     |                        |  |                |                |
| Lesson types and methods                    | Lesson type  | Lecture  | Tutorial  | Laboratory Project                  |                        | t  | Seminar        | SUM            |
| of instruction                              | Number of study hours  | 30.0   | 0.0   | 0.0                                 | 0.0                    |  | 0.0            | 30             |
|   | E-learning hours inclu   | ıded: 0.0  |   |                                     |                        |  |                |                |
| Learning activity and number of study hours | Learning activity  | Participation in didactic classes included in study plan |   | Participation in consultation hours |                        | Self-study   |                | SUM            |
|   | Number of study hours  | 30   |   | 2.0                                 |                        | 18.0   |                | 50             |
| Subject objectives                          | Learning the basics of design, operation and control of microcontrollers and their peripheral devices, and also electronic systems: digital buffers, parallel random access memories, SPLDs and CPLDs, selected systems controlled via the SPI interface.  Acquisition of the ability to analyze ("read") electronic block schemes and timings describing the behavior of the system at the time (work in "real time"), as well as effective learning skills of the technical documentation. |  |   |                                     |                        |  |                |                |
| Learning outcomes                           | Course outcome   |  | Subject outcome   |                                     | Method of verification |  |                |                |
|   | [K6_W03] knows and understands, to an advanced extent, the construction and operating principles of components and systems related to the field of study, including theories, methods and complex relationships between them and selected specific issues - appropriate for the curriculum   |  | Student defines the concept of distributed electronic microsystems. Student describes the principle of operation and control of systems that are part of electronic microsystems. |                                     |                        | [SW1] Assessment of factual knowledge                              |                |                |
|   | [K6_W04] knows and understands, to an advanced extent, the principles, methods and techniques of programming and the principles of computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study, and organisation of systems using computers or such devices   |  | Student explains the construction and principle of operation of the microcontroller and its peripherals. Student lists topologies and properties of serial interfaces.            |                                     |                        | [SW1] Assessment of factual knowledge                              |                |                |

Data wydruku: 18.07.2024 08:49 Strona 1 z 2

| Subject contents   | 1. Introduction, The plan of the lecture, definition of the embedded microcontroller 2. Definition of the microcontroller and features of the core processor 3. Addressing modes of the core processor 4. Classification of core processors taking into account a memory map (definition of the memory map) and an instruction set 5. The hardward architecture, the modified hardward architecture, the Von-Neumann architecture 6. RISC and CISC architectures of the core processor 7. Internal memories of microcontrollers (program and data memories) 8. Division of the microcontrollers regarding to a way of using of external memories 9. Microcontrollers with access to system buses through ports, with directly access to system buses, embedded microcontrollers 10. An stratified model of the embedded microcontroller 11. Families of the microcontroller 12. An oscillator circuit and circuits of generation and distribution of clock signals 13. Methods of power reduction and special modes of the microcontroller 14. Reset circuits of the microcontroller 15. Units supervising a work of the microcontroller: BOR, LVD. Circuits delaying the reset signal 16. The watchdog 17. An interrupt system with program polling of devices and a vector interrupt system 18. Parallel ports of the microcontroller - the layer of multiplexers and input/output pins 19. Overview and classification of peripheral devices of the microcontroller 20. Basic information about timers and counters 21. Configurations of timers: 16-bit counter/timer, Input Capture, Output Compare, One Pulse, PWM 22. Examples of the timers: timers in PIC16F877, ST72215G 23. Internal analog to digital converters 24. Internal analog comparators 25. Internal EEPROMs (configuration and service). Example of the EEPROM in AT90x8515 26. Characterization and division of serial interface contilers 27. The UART interface (building, principle of working, controlling) 28. Solutions of the UART interface in microcontrollers: 80C51/52, AT90x8515, PIC16F877 29. The SPI interface 30. Examples of the SPI i |  |                               |  |  |  |
|--|--|--|-------------------------------|--|--|--|
| Prerequisites and co-requisites                                | No requirements  |  |                               |  |  |  |
| Assessment methods   | Subject passing criteria   | Passing threshold  | Percentage of the final grade |  |  |  |
| and criteria   | Midterm colloquium   | 48.0%  | 100.0%                        |  |  |  |
| Recommended reading  | Supplementary literature   | wykładu, http://www.pg.gda.pl/~zbczaja, Gdańsk 2014.  Hadam P.: Projektowanie systemów mikroprocesorowych, Wyd. BTC, Warszawa 2004.  Bogusz J.: Lokalne interfejsy szeregowe w systemach cyfrowych, Wyd. BTC, Warszawa 2004.  Baranowski R.: Mikrokontrolery AVR ATmega w praktyce, Wyd. BTC, Warszawa 2005.  Jabłoński T: Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Warszawa 2002.  Jabłoński T., Pławsiuk K.: Programowanie mikrokontrolerów PIC w języku C, Wyd. BTC, Warszawa 2005.  Baranowski R.: Wyświetlacze graficzne i alfanumeryczne w systemach mikroprocesorowych, Wyd. BTC, Legionowo 2008. |                               |  |  |  |
|  | eResources addresses   |  |                               |  |  |  |
| Evernle issues/  | CINGSOULCES AUDICESSES   | Adresy na platformie eNauczanie:   |                               |  |  |  |
| Example issues/<br>example questions/<br>tasks being completed |  |  |                               |  |  |  |
| Work placement   | Not applicable   |  |                               |  |  |  |

Data wydruku: 18.07.2024 08:49 Strona 2 z 2