

Subject card

Subject name and code	Computer System Organization - laboratory, PG_00047622								
Field of study	Automatic Control, Cybernetics and Robotics								
Date of commencement of studies	October 2024		Academic year of realisation of subject			2026/2027			
Education level	first-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	3		Language of instruction			Polish			
Semester of study	6		ECTS credits			1.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Department of Autom	Department of Automatic Control -> Faculty of Electronics, Telecommunications and Informatics						S	
Name and surname	Subject supervisor		dr inż. Krzysztof Cisowski						
of lecturer (lecturers)	Teachers		dr inż. Krzysztof Cisowski						
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	t	Seminar	SUM	
of instruction	Number of study hours	0.0	0.0	15.0	0.0	0.0		15	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity	Participation in classes includ plan		Participation in consultation hours 1.0		Self-study		SUM	
	Number of study hours	15				9.0		25	
Subject objectives	The main aim of the subject is to gain knowledge about the most common computer systems organization and basic knowledge of computer system components and principles of their operation. The knowledge is applied for designing of computer PC programs.								
Learning outcomes	Course out	come	Subject outcome			Method of verification			
			The student describes and knows how to use in practice the design methods, in accordance with the given specification, and perform a simple device, object, system typical for the field of automation, or implement the process using appropriately selected methods, techniques, tools and materials, using engineering standards and norms, applying technologies relevant to the field of automation and using experience gained in an environment professionally engaged in engineering activities			[SU4] Assessment of ability to use methods and tools [SU3] Assessment of ability to use knowledge gained from the subject [SU1] Assessment of task fulfilment			
	techniques as well as select and apply appropriate programming		Student describes and put to use in practice elements of computer system architecture. Student describes and knows how to use in practice the elementary principles of programming a computer system. Student describes and knows how to use it in practice a system of inputs and outputs of the computer system. Student describes and put to use in practice the interrupt system. Student describes and put to use in practice PC computers and the PC BIOS programming module			[SU4] Assessment of ability to use methods and tools [SU1] Assessment of task fulfilment			

Data wydruku: 18.07.2024 08:17 Strona 1 z 2

Subject contents	The implementation of 6 projects in the form of computer programs written in any programming environment of computers PC. Topics of projects: model of software simulator of the microprocessor, extention the area of operation of the simulator by application of ten selected BIOS interrupts, program that uses the technique of creating a graphical user interface, application that simulates a serial communication compatible with the RS232 standard, "dependent on the time" application (eg . tester of human psychomotor skills), simulator of "production line" position of dispatcher.						
Prerequisites and co-requisites	Knowledge of the basic issues of digital technology and programming in one of languages: C++, C#, Visual C++ or Java etc.						
Assessment methods and criteria	Subject passing criteria	Passing threshold	Percentage of the final grade				
	Average rating of the projects	50.0%	100.0%				
Recommended reading	Basic literature	A. Pyrchla, B. Danowski, BIOS, Przewodnik, Helion 2007, A. S. Tanenbaum, Strukturalna organizacja systemów komputerowych, Helion 2006, R. Irvine, Asembler dla procesorów Intel, vademekum profesjonalisty, Helion 2003, Katalogi, Strony WWW					
	Supplementary literature _						
	eResources addresses	Adresy na platformie eNauczanie:					
Example issues/ example questions/ tasks being completed							
Work placement	Not applicable						

Data wydruku: 18.07.2024 08:17 Strona 2 z 2