

Subject card

Subject name and code	Design Methodology and Manufacturing - laboratory, PG_00048090								
Field of study	Electronics and Telecommunications								
Date of commencement of studies	October 2024		Academic year of realisation of subject			2026/2027			
Education level	first-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	3		Language of instruction			Polish			
Semester of study	6		ECTS credits			2.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Department of Metrology and Optoelectronics -> Faculty of Electronics, Telecommunications and Informatics								
Name and surname	Subject supervisor dr inż. Arkadiusz Szewczyk								
of lecturer (lecturers)	Teachers		dr inż. Arkadiusz Szewczyk						
Lesson types and methods of instruction	Lesson type	Lecture	Tutorial	Laboratory	Projec	:t	Seminar	SUM	
	Number of study hours	0.0	0.0	30.0	0.0		0.0	30	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity	Participation in classes include plan				Self-study		SUM	
	Number of study hours	30		2.0		18.0		50	
Subject objectives	Practical learning of technology of design and manufacturing of electronic equipment.								
Learning outcomes	Course outcome		Subject outcome			Method of verification			
	[K6_U07] can apply methods of process and function support, specific to the field of study		can use the methods of computer aided design and analysis of electronic devices			[SU4] Assessment of ability to use methods and tools			
	[K6_U06] can analyse the operation of components, circuits and systems related to the field of study, measure their parameters and examine technical specifications		is able to analyze the operation of the designed device and its components and measure their parameters and examine technical characteristics			[SU2] Assessment of ability to analyse information			
			is able to design, in accordance with the given specification, and build a simple electronic device			[SU1] Assessment of task fulfilment			

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Subject contents	Students in 2-3-person laboratory groups choose an electronic circuit for realisation (common for a laboratory group) with its electrical schematic and requirements, functional description and basic el parameters. The introductory exercise is realised by all the students and gives a possibility of learni current version of software used and is performed strictly according to the description in the laboratinstruction. The results of this exercise are acquired to the laboratory data base.						
	Then students collect components necessary for the realisation of a chosen circuit, define its basic electrical and technological parameters necessary in the designing.						
	The scope of the laboratory program contains accomplishment using software PADS v. 9.5 of Mentor Graphics a schematic, netlist, bill of materials and PCB design together with appropriate reports. After the PCB realisation students make mounting of a chosen circuit (in the SMD or through-holes technology), prepare a program of measurements. The following circuits are available for selection::						
	 Low-noise transistor amplifier Generator of rectangular and triangular waveforms Voltage-frequency converter Power supply with feedback for +15 V Acoustic amplifier 2 x 10 W Circuit with AT89C2051 microcontroller for LED diodes Circuit with AT89C2051 microcontroller for 7-segment display Tone generator Universal power module Bandpass filter with multi-feedback 						
Prerequisites and co-requisites	For a proper realisation of tasks in the laboratory program, students should use a knowledge acquired from the following subjects: <i>Methods and techniques of designing and realisation</i> (lecture), <i>Material engineering</i> , <i>Metrology and technique of an experiment</i> , Analog and digital techniques.						
Assessment methods and criteria	Subject passing criteria	Passing threshold	Percentage of the final grade				
	Individual and group reports, realised subassembly	50.0%	100.0%				
Recommended reading	Basic literature	 Laboratory instruction for PADS software. Laboratory documentation of software and auxiliary files from producers – <i>User Manuals, Tutorials, data sheets.</i> Spiralski L., Konczakowska A.: Basics of technology and construction of electronic equipment and systems. WSM, Gdynia 1997. In Polish. 4. Stępień St. and all: Guide for a constructor of electronic equipment. WKiŁ, Warszawa 1981. In Polish. 					
	Supplementary literature	Brak					
	eResources addresses Adresy na platformie eNauczanie:						
Example issues/ example questions/ tasks being completed	Initial determination of basic electrical parameters and design and technology elements which knowledge is required in the design phase.						
Work placement	Not applicable						

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