

。 GDAŃSK UNIVERSITY OF TECHNOLOGY

Subject card

Subject name and code	Microcontrollers and Microsystems, PG_00048074								
Field of study	Electronics and Teleo	communication	s						
Date of commencement of studies	October 2025		Academic year of realisation of subject			2027/2028			
Education level	first-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of de	livery		at the	at the university		
Year of study	3		Language of instruction			Polish			
Semester of study	5		ECTS credits			3.0			
Learning profile	general academic profile		Assessmer	Assessment form			assessment		
Conducting unit	Department Of Metrology And Optoelectronics -> Faculty Of Electronics Telecommunications And Informatics -> Wydziały Politechniki Gdańskiej							And	
Name and surname	Subject supervisor	dr hab. inż. Zt	oigniew Czaja						
of lecturer (lecturers)	Teachers		dr hab. inż. Zbigniew Czaja						
Lesson types and methods of instruction	Lesson type	Lecture	Tutorial	Laboratory	Projec	:t	Seminar	SUM	
	Number of study hours	30.0	0.0	15.0	0.0		0.0	45	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity	Participation i classes incluc plan		Participation in consultation hours		Self-study		SUM	
	Number of study hours	45		3.0		27.0		75	
	systems controlled via the SPI interface. Acquisition of the ability to analyze ("read") electronic block schemes and timings describing the behavior of the system at the time (work in "real time"), as well as effective learning skills of the technical documentation.								
Learning outcomes	Course outcome		Subject outcome			Method of verification			
	[K6_U04] can apply knowledge of programming methods and techniques as well as select and apply appropriate programming methods and tools in computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study		Student explains the construction and principle of operation of the			[SU1] Assessment of task fulfilment [SU2] Assessment of ability to analyse information			
			Student describes the principle of operation and control of systems that are part of electronic microsystems. Student analyzes program codes written in an assembler and a C language written for microcontrollers.			[SW1] Assessment of factual knowledge			

		lecture, definition of the microcontroll				
Subject contents	processor 2. Addressing modes of the core processor 3. Classification of microcontrollers taking into account a memory may (definition of the memory map) and an instruction set 4. Features of the harward architecture and its modifications, proprieties of the Von-Neumann architecture in microcontrollers 5. RISC and CISC architectures of the core processor 6. Internal memories of microcontrollers (program and data memories) 7. Division of the microcontrollers regarding to a way of using of external memories 8. Microcontrollers with access to system buses through ports, with directly access to system buses, embedded microcontrollers 9. A stratified model of the embedded microcontroller 10. Classification and division of families of the microcontrollers 11. Building of an oscillator circuit and applications of circuits of generation and distribution of clock signals 12. Ways of reductions of power consumption and saving power modes of the microcontroller 13. Reset blocks of the microcontroller 14. Units supervising executing of programs by the microcontroller 19. Basic information about timers and counters 20. Configurations of timers: 16-bit counter/timer, input Capture, Output Compare, One Pulse, PVM 21. Examples of the timers: timers in PIC18F452, ST72215G 22. Internal analog to digital converters 23. Internal analog comparators 24. Internal EPROMS (configuration and service). Example of the EEPROM in Atmega16 25. Characterization and division of serial interface an incorcontrollers: Atmega16, PIC18F452 20. The SPI interface service 29. Solutions of the SPI interface in the microcontrollers: Atmega16, PIC18F452 20. The SPI interface service 29. Solutions of 12C, CAN, USB interfaces 32. The parallel interface PSI 33. Types of packages of the embedded microcontrollers 34. Definition of an embedded programming 35. Programming of the core processor in an assembler language 36. Writing programs in high level languages 37. Activating of programs writen for microcosystems 40. Standards of serial inte					
Prerequisites and co-requisites	No requirements					
•			Demonstration (1) (1)			
Assessment methods and criteria	Subject passing criteria	Passing threshold	Percentage of the final grade			
	Practical exercise	50.0%	40.0%			
	Midterm colloquium	48.0%	60.0%			
Recommended reading	Basic literature Czaja Z.: Mikrokontrolery i mikrosystemy – materiały do wykładu, www.pg.gda.pl/~zbczaja, Gdańsk 2010. Hadam P.: Projektowanie systemów mikroprocesorowych, Wyd. BTC, Warszawa 2004.					
	Supplementary literature Bogusz J.: Lokalne interfejsy szeregowe w systemach cyfrowych, W BTC, Warszawa 2004. Baranowski R.: Mikrokontrolery AVR ATmega praktyce, Wyd. BTC, Warszawa 2005. Jabłoński T: Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Warszawa 2002. Jabłoński T., Pławsiuk K.: Programowanie mikrokontrolerów PIC w języku C, Wyd BTC, Warszawa 2005. Baranowski R.: Wyświetlacze graficzne i alfanumeryczne w systemach mikroprocesorowych, Wyd. BTC, Legionowo 2008.					
	eResources addresses	Adresy na platformie eNauczanie:				
Example issues/ example questions/ tasks being completed		·				
Work placement	Not applicable					

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