

Subject card

Subject name and code	Microprocessors and Controllers, PG_00047831								
Field of study	Biomedical Engineering								
Date of commencement of studies	October 2025		Academic year of realisation of subject			2027/2028			
Education level	first-cycle studies		Subject group			Optional subject group Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	3		Language of instruction			Polish			
Semester of study	5		ECTS credits			4.0			
Learning profile	general academic profile		Assessment form			exam			
Conducting unit	Department Of Metrology And Optoelectronics -> Faculty Of Electronics Telecommunications And Informatics -> Wydziały Politechniki Gdańskiej					And			
Name and surname	Subject supervisor		dr hab. inż. Zbigniew Czaja						
of lecturer (lecturers)	Teachers	dr hab. inż. Zbigniew Czaja							
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	:t	Seminar	SUM	
of instruction	Number of study hours	30.0	0.0	15.0	0.0		0.0	45	
	E-learning hours included: 0.0								
Learning activity and number of study hours	earning activity Participation in classes includ plan				Self-study SUM		SUM		
	Number of study hours	45	4.0			51.0		100	
Subject objectives	Learning the basics of design, operation and control of microprocessors, microcontrollers and their peripheral devices, and also electronic systems: digital buffers, RAM and FLSAH memories, selected systems controlled via the SPI interface. Acquisition of the ability to analyze ("read") electronic block schemes and timings describing the behavior of the system at the time (work in "real time"), as well as effective learning skills of the technical documentation.								
Learning outcomes	Course out	come	Subject outcome			Method of verification			
[K6_W04] knows at understands, to an extent, the principle and techniques of p and the principles of software development programming device controllers using mor programmable e systems specific to study, and organisal systems using complexication.		dvanced s, methods ogramming computer nt or s or roprocessors ments or he field of ion of	Student explains the structure and principle of microprocessors. Student describes the principle of operation and software of microcontrollers. Student uses IDE software for compilation, program simulation and programming of microcontrollers. Student creates software in assembler and C language for microcontrollers.		[SW1] Assessment of factual knowledge				
	[K6_U02] can perform tasks related to the field of study in an innovative way as well as solve complex and nontypical problems, applying knowledge of physics, in changing and not fully predictable conditions		Student is able to analyze block and logic diagrams describing complex integrated electronic circuits. The student knows how to analyze timings describing the behavior of digital systems in time.			[SU1] Assessment of task fulfilment [SU2] Assessment of ability to analyse information			

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	Introduction. Basics: microprocessor, microcomputer, microcomputer system 2. Functional components of microprocessor: arithmetical-logical unit, instruction pointer, specificity of general purpose and dedicated regis-ters of microprocessor, control unit 3. Address, data and control busses of microprocessor 4. A microprocessor memory map. Von Neumann and harward architectures 5. Types and parameters of memories used in microprocessor systems 6. EEPROM, FLASH, FRAM, MRAM nonvolatile memories 7. Cooperation of the microprocessor with external memories. Address decoders. Read/Write bus timings for SRAM and DRAM memories 8. Stack in data memory versus hardware stack 9. Direct memory access. DMA controller 10. Machine cycle of microprocessor. Addressing modes 11. Instruction types, microprocessors assembler instruction syntax. Macroassemblers 12. Advantages of RISC versus CISC microprocessors 13. Instructions for arithmetic and logical operations, data movement, conditional and unconditional jump, stack management 14. Microprocessor communication with external circuits. Programmable, universal and specialized input-output circuits 15. Interrupt system of microprocessor. External and internal interrupts. Interrupt masking. Interrupt servicing 16. Co-processor. Architecture, basic operations 17. Methods of elargement of computing power of microprocessors. Pipeline work. VLIW and EPIC architectures. 18. Multiplot. Multicore. 19. Acceleration of access to memories. Cache memory. Methods of writing and reading to/from cache memory. 20. Comparison of modern microprocessor families (ARM, PowerPC, MIPS) to Intel architecture 21. Definition, architecture and applications of microcontrollers (22. Families of microcontrollers 23. A stratified model of the microcontroller 28. Unitanal memories of microcontrollers 27. Reset blocks of the microcontroller 28. Units supervising executing of generation and distribution of clock signals 26. Ways of reductions of power consumption and saving power modes of the microcontroller 32.						
and co-requisites	· 						
Assessment methods	Subject passing criteria	Passing threshold	Percentage of the final grade				
and criteria	Practical exercise	50.0%	40.0%				
	Written examination	45.0%	60.0%				
r toodininon dod rodding	Supplementary literature	Czaja Z.: Mikroprocesory i mikrokontrolery – materiały do wykładu, http://www.pg.gda.pl/~zbczaja, Gdańsk 2014. Michalski J. A.: Mikroklocki. Mikroprocesory dla początkujących, Wyd. BTC, Warszawa 2007. Krzyżanowski R.: Układy mikroprocesorowe, Wyd. PWN 2007, Baranowski R.: Mikrokontrolery AVR ATmega w praktyce, Wyd. BTC, Warszawa 2005. Jabłoński T: Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Warszawa 2002.					
			F8x w praktyce, Wyd. BTC,				
	eResources addresses		owanie mikrokontrolerów PIC w 05.				
Example issues/ example questions/ tasks being completed	eResources addresses	Warszawa 2002. Jabłoński T., Pławsiuk K.: Programo języku C, Wyd. BTC, Warszawa 200 Bryndza L.: LPC2000 - Mikrokontrol Legionowo 2007.	owanie mikrokontrolerów PIC w 05.				

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