

GDAŃSK UNIVERSITY

Subject card

Subject name and code	Introduction to electronics and electrotechnics, PG_00051068								
Field of study	Technical Physics								
Date of commencement of studies			Academic year of realisation of subject			2026/2027			
Education level	first-cycle studies		Subject group			Obligatory subject group in the field of study			
						Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	2		Language of instruction			Polish			
Semester of study	4		ECTS credits			5.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Zakład właściwości magnetycznych i elektrycznych materiałów -> Institute Of Nanotechnology And Materials Engineering -> Faculty Of Applied Physics And Mathematics -> Wydziały Politechniki Gdańskiej								
Name and surname	Subject supervisor								
of lecturer (lecturers)	Teachers		ļ						
Lesson types and methods of instruction	Lesson type	Lecture	Tutorial	Laboratory	Projec	t	Seminar	SUM 60	
	Number of study hours	30.0	0.0	15.0	15.0		0.0	60	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity	Participation in classes includ		Participation in consultation hours		Self-study		SUM	
	Number of study hours	60		5.0		60.0		125	
Subject objectives	The aim of the course is to teach students the basics of electronics and electrical engineering, as well as basic skills in the design and testing of simple electronic circuits.								
Learning outcomes	Course outcome		Subject outcome			Method of verification			
	[K6_U05] designs and builds a simple measuring device		He or she can design and test an analog circuit fulfilling a specific function in a simulation environment.			[SU5] Assessment of ability to present the results of task			
	[K6_U06] makes an initial economic analysis of undertaken engineering activities					[SU2] Assessment of ability to analyse information			
	[K6_W06] has knowledge of electronics					[SW1] Assessment of factual knowledge			
	[K6_W07] has knowledge of the construction and operation of physical instruments, measurement and research equipment					[SW1] Assessment of factual knowledge			
Subject contents	 Basics laws of electricity and electronic components Classification of electronic components Schemes of electronic circuits DC electronic circuits AC electronic circuits Basic passive components (RLC) Active components Semiconductors Diodes Transistors Special semiconductor devices Manufacturing of semiconductor devices Integrated circuits Safe exploitation of electrical devices 								

Prerequisites	No prerequisites						
and co-requisites							
Assessment methods	Subject passing criteria	Passing threshold	Percentage of the final grade				
and criteria	Test of knowledge about instruments used in testing electrical circuits placed on the e- course (15 min.)	51.0%	5.0%				
	Report presenting the results of the project	51.0%	15.0%				
	Assessment of the implementation of laboratory exercises	51.0%	20.0%				
	Cost estimate for the purchase of elements for the construction of the designed electronic circuit	51.0%	5.0%				
	Final exam (90 min.)	51.0%	50.0%				
	Report on the simulation of an electrical circuit	51.0%	5.0%				
Recommended reading	Basic literature	 2015. W. Opydo, Elektrotechnika i elenieelektrycznych, Wyd. Politec 	WSiP, Warszawa, 2006. arcinkowski, Podstawy d. Politechniki Gdańskiej, Gdańsk, ektronika dla studentów wydziałów hniki Poznańskiej, Poznań, 2005. zanie: https://enauczanie.pg.edu.pl/				
	Supplementary literature	 P. Hempowicz et al., Elektrotechnika i elektronika dla nieelektryków, WN-T, Warszawa, 1999. P. Horowitz, W. Hill, Sztuka elektroniki 1, WKŁ, Warszawa, 2018. M. Polowczyk, A. Jurewicz, Elektronika dla mechaników, Wyd. Politechniki Gdańskiej, Gdańsk 2002. R. Śledziewski, Elektronika dla fizyków, PWN, Warszawa, 1982. 					
	eResources addresses	Resources addresses Adresy na platformie eNauczanie:					
Example issues/ example questions/ tasks being completed	 Describe nad illustrate Kirchhoff's first law. Build an RC low pass filter and determine its cut-off frequency. Design, build and perform tests of a rumble metal detector. 						
Work placement	Not applicable						

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