

Subject card

Subject name and code	IoT Hardware Platforms, PG_00064088								
Field of study	Electronics and Telecommunications								
Date of commencement of studies	February 2026		Academic year of realisation of subject			2025/2026			
Education level	second-cycle studies		Subject group			Optional subject group Specialty subject group Subject group related to scientific research in the field of study			
Mode of study	Full-time studies		Mode of delivery			at the university			
Year of study	1		Language of instruction			Polish			
Semester of study	1		ECTS credits			2.0			
Learning profile	general academic profile		Assessment form			assessment			
Conducting unit	Department Of Metrology And Optoelectronics -> Faculty Of Electronics Telecommunications And Informatics -> Wydziały Politechniki Gdańskiej						And		
Name and surname	Subject supervisor		dr inż. Andrzej Kwiatkowski						
of lecturer (lecturers)	Teachers		dr inż. Andrze	dr inż. Andrzej Kwiatkowski					
Lesson types and methods	Lesson type	Lecture	Tutorial	Laboratory	Projec	:t	Seminar	SUM	
of instruction	Number of study hours	15.0	0.0	15.0	0.0		0.0	30	
	E-learning hours included: 0.0								
Learning activity and number of study hours	Learning activity Participation in classes include plan				Self-study SUM				
	Number of study hours	30		2.0		18.0		50	
Subject objectives	The aim of the course is to familiarize students with the structure of popular hardware platforms used in Internet of Things (IoT) systems, wired and wireless interfaces used in IoT devices, popular communication modules, data storage methods, issues of software minimization of energy consumption and hardware-assisted methods of information security.								
Learning outcomes	Course outcome		Subject outcome			Method of verification			
	[K7_W03] knows and understands, to an increased extent, the construction and operating principles of components and systems related to the field of study, including theories, methods and complex relationships between them and selected specific issues - appropriate for the curriculum		He knows and distinguishes the architectures of IoT systems, he is able to choice them depending on the required computing power and available energy source. He understands the need to secure information. He knows the methods of storing data and how to use communication modules			[SW1] Assessment of factual knowledge			
	[K7_U04] can apply knowledge of programming methods and techniques as well as select and apply appropriate programming methods and tools in computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study, making assessment and critical analysis of the prepared software as well as a synthesis and creative interpretation of information presented with it		He is able to select and configure a hardware platform depending on needs. Selects an appropriate interface and communication module depending on the required bandwidth and amount of data. Is able to configure a system that stores data locally and in the cloud. Is able to use design tools dedicated to the selected hardware platform.			[SU1] Assessment of task fulfilment [SU4] Assessment of ability to use methods and tools			

Prerequisites	Basic concepts and definitions.2. Classification of data processing units (microcontrollers, microprocessors, SoC, SBC).3. Wired communication interfaces in IoT.4. Wireless communication interfaces in IoT.5. Data storage.6. Overview of typical hardware platforms.7. Overview of selected communication modules; 8. Single board computers as an IoT platform.9. Minimizing energy consumption - special operating modes; 10. Data security issues in IoT. Basic knowledge of digital circuit, microprocessors and microcontrollers, and programming in C.						
and co-requisites Assessment methods	Subject passing criteria	Passing threshold	Percentage of the final grade				
and criteria	Laboratory exercises	50.0%	50.0%				
	Semester test	50.0%	50.0%				
Recommended reading	Basic literature	rated circuits presented during the atthings - IoTs, Pacific 19 processor Systems: Real stems Design: An Introduction services and Connected IoT Gateway with selling no Overview fejsy cyfrowe anie XI 216-01 I2C MANUAL, 2003 fication and user manual, 2014 SPI Interface, Analogue Dialog ny interfejs szeregowy.					
	Supplementary literature	Ed Sutter, Embedded Systems Firmware Demystified Michael Barr, Programming Embedded Systems in C and C ++ Stuart R. Ball; Debugging Embedded Microprocessor Systems,					
	eResources addresses	Adresy na platformie eNauczanie:					
Example issues/ example questions/ tasks being completed							
Work placement	Not applicable						

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