



Subject card

|  |  |  |   |                                     |                                       |                        |     |
|--|--|--|---|-------------------------------------|---------------------------------------|------------------------|-----|
| Subject name and code  | Basic Electronic Circuits, PG_00068279   |  |   |                                     |                                       |                        |     |
| Field of study   | Biomedical Engineering, Biomedical Engineering, Biomedical Engineering   |  |   |                                     |                                       |                        |     |
| Date of commencement of studies  | October 2026   | Academic year of realisation of subject  |   |                                     |                                       | 2027/2028              |     |
| Education level  | first-cycle studies  | Subject group  |   |                                     |                                       | Optional subject group |     |
| Mode of study  | Full-time studies  | Mode of delivery   |   |                                     |                                       | at the university      |     |
| Year of study  | 2  | Language of instruction  |   |                                     |                                       | Polish                 |     |
| Semester of study  | 4  | ECTS credits   |   |                                     |                                       | 3.0                    |     |
| Learning profile   | general academic profile   | Assessment form  |   |                                     |                                       | assessment             |     |
| Conducting unit  | Department of Microelectronic Systems -> Faculty of Electronics Telecommunications and Informatics -> Faculties of Gdańsk University of Technology   |  |   |                                     |                                       |                        |     |
| Name and surname of lecturer (lecturers)   | Subject supervisor   |  | dr hab. inż. Bogdan Pankiewicz  |                                     |                                       |                        |     |
|  | Teachers   |  | dr hab. inż. Bogdan Pankiewicz  |                                     |                                       |                        |     |
| Lesson types   | Lesson type  | Lecture  | Tutorial  | Laboratory                          | Project                               | Seminar                | SUM |
|  | Number of study hours  | 15.0   | 15.0  | 0.0                                 | 0.0                                   | 0.0                    | 30  |
|  | E-learning hours included: 0.0   |  |   |                                     |                                       |                        |     |
| Learning activity and number of study hours  | Learning activity  | Participation in didactic classes included in study plan                             |   | Participation in consultation hours |                                       | Self-study             | SUM |
|  | Number of study hours  | 30   |   | 2.0                                 |                                       | 43.0                   | 75  |
| Subject objectives   | Celem przedmiotu jest zapoznanie uczestników z budową, zasadami działania oraz analizy podstawowych analogowych układów elektronicznych, zarówno liniowych jak i nieliniowych. Omówione są zagadnienia wykorzystania tranzystorów bipolarnych i MOS w budowie podstawowych układów elektronicznych. W ramach kursu studenci również poznają budowę i właściwości rzeczywistych wzmacniaczy operacyjnych oraz nauczą się wykorzystywać je w konstrukcji szerokiej gamy układów elektronicznych takich jak wzmacniacze instrumentacyjne, filtry aktywne czy generatory drgań harmonicznnych. |  |   |                                     |                                       |                        |     |
| Learning outcomes  | Course outcome   |  | Subject outcome   |                                     | Method of verification                |                        |     |
|  | [K6_W04] knows and understands, to an advanced extent, the principles, methods and techniques of programming and the principles of computer software development or programming devices or controllers using microprocessors or programmable elements or systems specific to the field of study, and organisation of systems using computers or such devices   |  | The student knows the structure and parameters of basic linear and nonlinear analog electronic circuits, as well as their applications. |                                     | [SW1] Assessment of factual knowledge |                        |     |
| [K6_U12] can analyze the operation of components, circuits and systems related to the field of study, as well as measure their parameters and examine technical specifications, and plan and conduct experiments related to the field of study, including computer simulations and measurements, and interpret obtained results and draw conclusions |  | The student is able to calculate the parameters of basic analog electronic circuits. |   | [SU1] Assessment of task fulfilment |                                       |                        |     |

| Subject contents   | <p>Course content – lecture</p> <p>The lecture will cover the following topics: 1. DC characteristics of bipolar and field-effect transistors and their small-signal equivalent models. 2. Biasing circuits for transistor amplifiers. 3. Analysis and characteristics of bipolar and MOS transistor amplifiers in basic configurations. 4. Frequency characteristics of broadband transistor amplifiers. 5. Structure and parameters of operational amplifiers. 6. Application of operational amplifiers in building basic electronic circuits. 7. Use of negative feedback. 8. Analysis of the nonlinear properties of bipolar amplifiers, MOS amplifiers, and differential pairs. 9. Multiplier circuits. 10. Implementation of selected nonlinear functions. 11. RC harmonic oscillators. 12. Schmitt trigger. 13. Relaxation oscillators.</p> <p>As part of the course exercises, students will solve problems corresponding to the lecture content. During each exercise session, students are expected to solve two or three calculation-based problems.</p>  |                               |  |                          |  |                               |                          |   |       |                                      |       |       |
|--|--|-------------------------------|--|--------------------------|--|-------------------------------|--------------------------|---|-------|--------------------------------------|-------|-------|
| Prerequisites and co-requisites                                | Basic knowledge of circuit theory and the properties of electronic components.   |                               |  |                          |  |                               |                          |   |       |                                      |       |       |
| Assessment methods and criteria                                | <table border="1" data-bbox="448 461 1487 584"> <thead> <tr> <th data-bbox="448 461 794 495">Subject passing criteria</th> <th data-bbox="794 461 1141 495">Passing threshold</th> <th data-bbox="1141 461 1487 495">Percentage of the final grade</th> </tr> </thead> <tbody> <tr> <td data-bbox="448 495 794 528">Homework and attendance</td> <td data-bbox="794 495 1141 528">50.0%</td> <td data-bbox="1141 495 1487 528">50.0%</td> </tr> <tr> <td data-bbox="448 528 794 584">Midterm tests during class sessions.</td> <td data-bbox="794 528 1141 584">50.0%</td> <td data-bbox="1141 528 1487 584">50.0%</td> </tr> </tbody> </table>  |                               |  | Subject passing criteria | Passing threshold  | Percentage of the final grade | Homework and attendance  | 50.0%   | 50.0% | Midterm tests during class sessions. | 50.0% | 50.0% |
| Subject passing criteria                                       | Passing threshold  | Percentage of the final grade |  |                          |  |                               |                          |   |       |                                      |       |       |
| Homework and attendance  | 50.0%  | 50.0%                         |  |                          |  |                               |                          |   |       |                                      |       |       |
| Midterm tests during class sessions.                           | 50.0%  | 50.0%                         |  |                          |  |                               |                          |   |       |                                      |       |       |
| Recommended reading  | <table border="1" data-bbox="448 591 1487 857"> <tbody> <tr> <td data-bbox="448 591 794 719">Basic literature</td> <td colspan="2" data-bbox="794 591 1487 719">           1) Guziński A: "Liniowe elektroniczne układy analogowe", WNT, 1994.<br/>           2) Sedra A.S., Smith K.C.: "Microelectronic circuits", Oxford University Press, New York, Oxford, 2020.<br/>           3) Niedźwiecki M., Rasiukiewicz M.: "Nieliniowe elektroniczne układy analogowe", WNT 1991.         </td> </tr> <tr> <td data-bbox="448 719 794 824">Supplementary literature</td> <td colspan="2" data-bbox="794 719 1487 824">           1) Soclof S.: "Design and Application of Analog Integrated Circuits", Prentice Hall, 1996.<br/>           2) Tietze U., Schenk Ch.: "Electronic Circuits --- Handbook for Design and Applications", Springer 2nd edition, 2008.         </td> </tr> <tr> <td data-bbox="448 824 794 857">eResources addresses</td> <td colspan="2" data-bbox="794 824 1487 857"></td> </tr> </tbody> </table>   |                               |  | Basic literature         | 1) Guziński A: "Liniowe elektroniczne układy analogowe", WNT, 1994.<br>2) Sedra A.S., Smith K.C.: "Microelectronic circuits", Oxford University Press, New York, Oxford, 2020.<br>3) Niedźwiecki M., Rasiukiewicz M.: "Nieliniowe elektroniczne układy analogowe", WNT 1991. |                               | Supplementary literature | 1) Soclof S.: "Design and Application of Analog Integrated Circuits", Prentice Hall, 1996.<br>2) Tietze U., Schenk Ch.: "Electronic Circuits --- Handbook for Design and Applications", Springer 2nd edition, 2008. |       | eResources addresses                 |       |       |
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| eResources addresses   |  |                               |  |                          |  |                               |                          |   |       |                                      |       |       |
| Example issues/<br>example questions/<br>tasks being completed | <ol style="list-style-type: none"> <li>1. Calculate the operating point of an amplifier with a bipolar or MOS transistor.</li> <li>2. Calculate the values of the parameters of the transistor's small-signal equivalent model.</li> <li>3. Draw the small-signal equivalent circuit diagram of a transistor amplifier for mid-frequency range.</li> <li>4. Calculate the small-signal voltage gain as well as the input and output resistance of the transistor amplifier.</li> <li>5. Draw the small-signal equivalent circuit diagram of the amplifier for low frequencies.</li> <li>6. Draw the small-signal equivalent circuit diagram of the amplifier for high frequencies.</li> <li>7. Calculate the cutoff frequencies of the transistor amplifier.</li> <li>8. Apply Millers theorem to determine the upper cutoff frequency of the transistor amplifier.</li> <li>9. List the parameters of ideal and real operational amplifiers.</li> <li>10. Provide circuit diagrams and parameters of systems using operational amplifiers.</li> <li>11. List the properties of negative feedback.</li> <li>12. Describe the properties of transistor amplifiers operating with signals that cause small harmonic distortion.</li> <li>13. List the properties of a bipolar differential pair.</li> <li>14. Describe the Gilbert multiplier circuit.</li> <li>15. Provide circuit diagrams of basic systems with operational amplifiers implementing simple nonlinear functions.</li> <li>16. Explain the principles of analyzing harmonic oscillators.</li> <li>17. Provide the circuit diagram and properties of a Schmitt trigger.</li> </ol> |                               |  |                          |  |                               |                          |   |       |                                      |       |       |
| Practical activities within the subject                        | Not applicable   |                               |  |                          |  |                               |                          |   |       |                                      |       |       |

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